



6N40-C

Preliminary

Power MOSFET

6.0A, 400V N-CHANNEL POWER MOSFET

DESCRIPTION

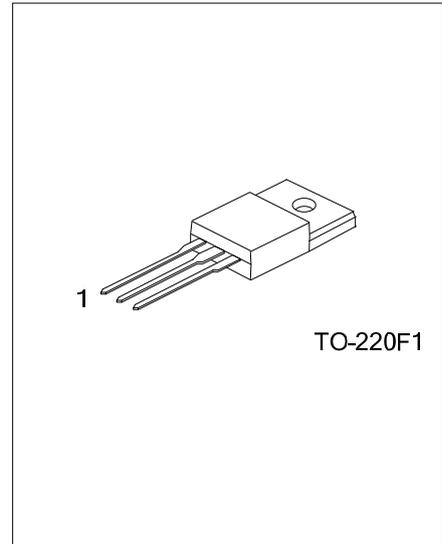
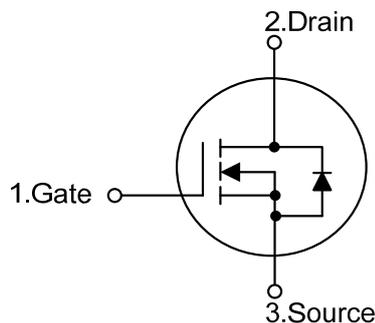
The UTC **6N40-C** is an N-Channel enhancement mode power MOSFET using UTC's perfect planar stripe, DMOS technology to provide customers with superior switching performance and minimum on-state resistance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **6N40-C** is generally used in applications, such as electronic lamp ballasts based on half bridge topology and high efficiency switched mode power supplies.

FEATURES

- * $R_{DS(ON)} < 1.2\Omega @ V_{GS}=10V, I_D=3.0A$
- * Fast switching speed
- * Improved dv/dt capability

SYMBOL



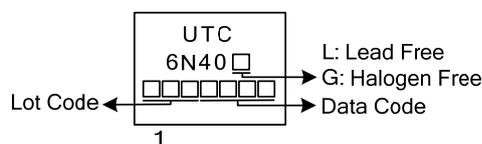
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6N40L-TF1-T	6N40G-TF1-T	TO-220F1	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>6N40L-TF1-T</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube (2) TF1: TO-220F1 (3) L: Lead Free, G: Halogen Free and Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	400	V
Gate-Source Voltage		V_{GSS}	± 30	V
Avalanche Current (Note 2)		I_{AR}	6	A
Drain Current	Continuous	I_D	6	A
	Pulsed (Note 2)	I_{DM}	24	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	150	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5	V/ns
Power Dissipation		P_D	38	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature
3. $L=8.3\text{mH}$, $I_{AS}=6.0\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$
4. $I_{SD} \leq 6.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	θ_{JC}	3.3	$^\circ\text{C}/\text{W}$

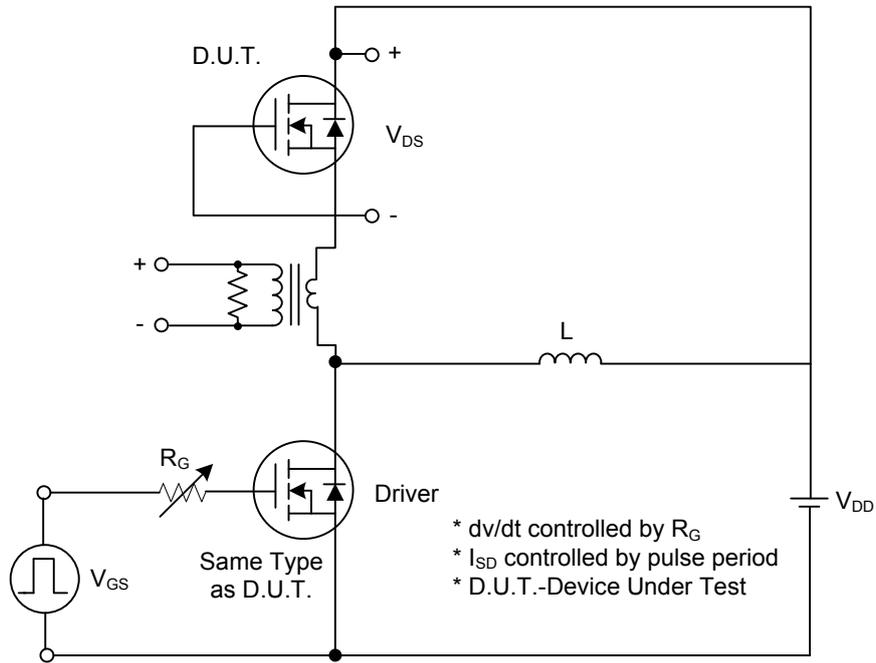
■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	400			V	
Breakdown Voltage Temperature Coefficient		ΔBV _{DSS} /ΔT _J	I _D =250μA, Referenced to 25°C		0.54		V/°C	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μA	
			V _{DS} =320V, T _J =125°C			10	μA	
Gate-Source Leakage Current	Forward	I _{GSS}	V _{DS} =0V, V _{GS} =+30V			+100	nA	
	Reverse		V _{DS} =0V, V _{GS} =-30V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V	
Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =3.0A			1.2	Ω	
DYNAMIC PARAMETERS								
Input Capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		513		pF	
Output Capacitance		C _{OSS}				75		pF
Reverse Transfer Capacitance		C _{RSS}				8		pF
SWITCHING PARAMETERS								
Total Gate Charge		Q _G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A, I _G =100μA (Note 1, 2)		16		nC	
Gate-Source Charge		Q _{GS}				6		nC
Gate-Drain Charge		Q _{GD}				3.2		nC
Turn-ON Delay Time		t _{D(ON)}	V _{DD} =30V, V _{GS} =10V, I _D =0.5A, R _G =25Ω (Note 1, 2)		45		ns	
Turn-ON Rise Time		t _R				57		ns
Turn-OFF Delay Time		t _{D(OFF)}				91		ns
Turn-OFF Fall Time		t _F				36		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Body-Diode Continuous Current		I _S				6	A	
Maximum Body-Diode Pulsed Current		I _{SM}				24	A	
Drain-Source Diode Forward Voltage		V _{SD}	I _S =6A, V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery Time		t _{rr}	V _{GS} =0V, I _S =6A,		230		ns	
Body Diode Reverse Recovery Charge		Q _{RR}	di _F /dt=100A/μs (Note 1)		1.4		μC	

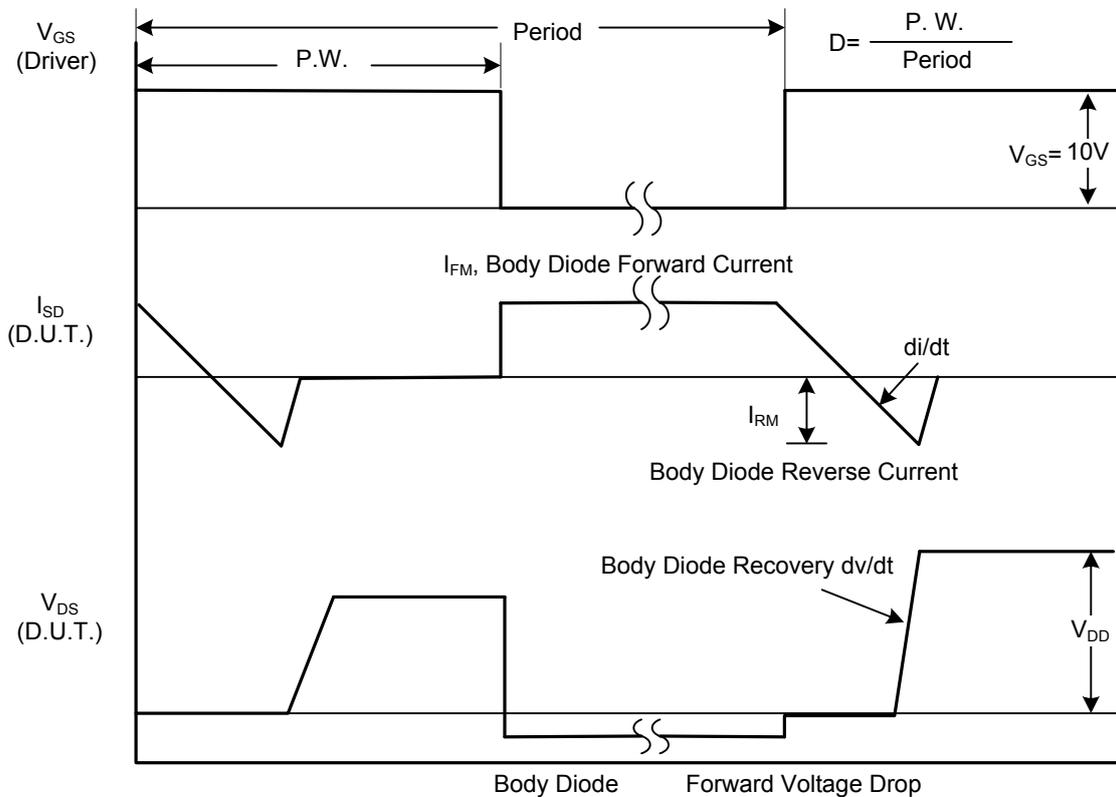
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

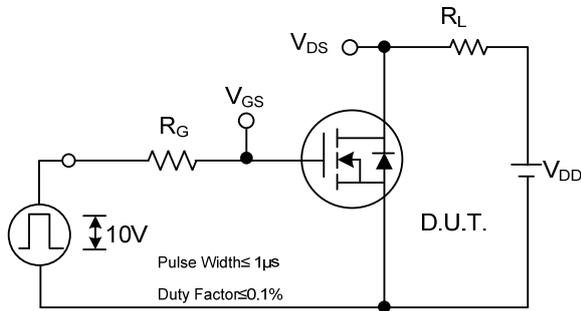


Peak Diode Recovery dv/dt Test Circuit

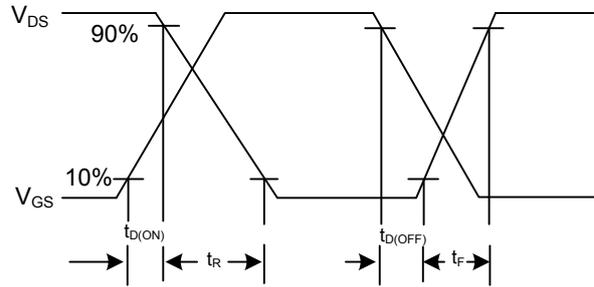


Peak Diode Recovery dv/dt Waveforms

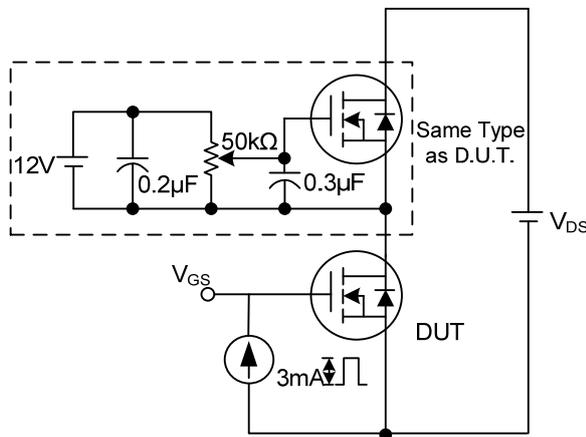
■ TEST CIRCUITS AND WAVEFORMS(Cont.)



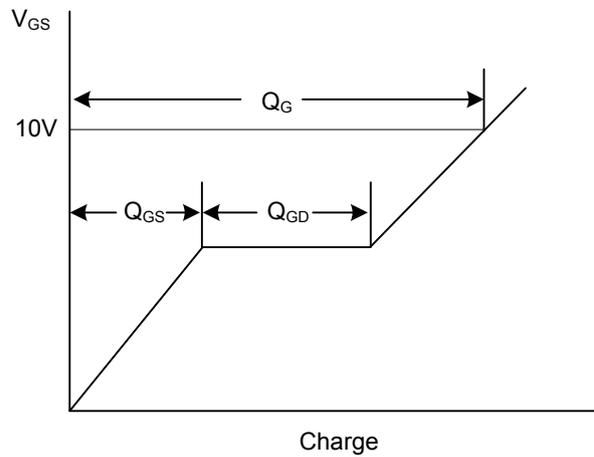
Switching Test Circuit



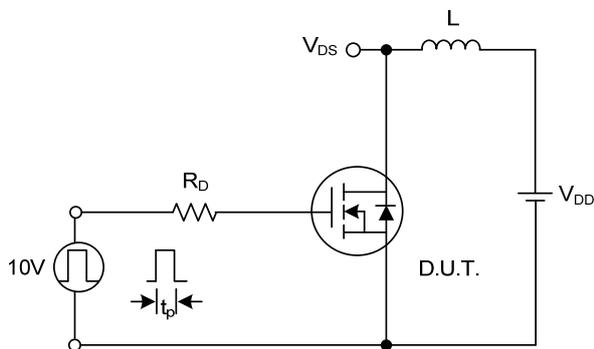
Switching Waveforms



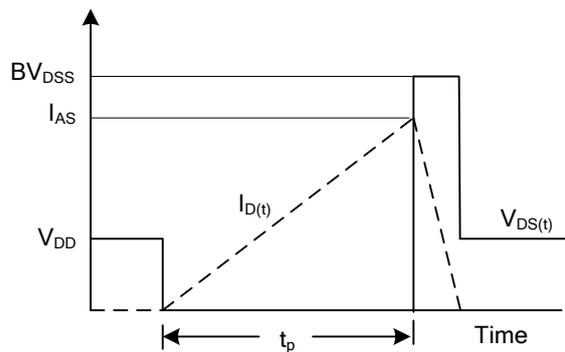
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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