



U74AHC125

CMOS IC

QUADRUPLE BUS BUFFER GATE WITH 3-STATE OUTPUTS

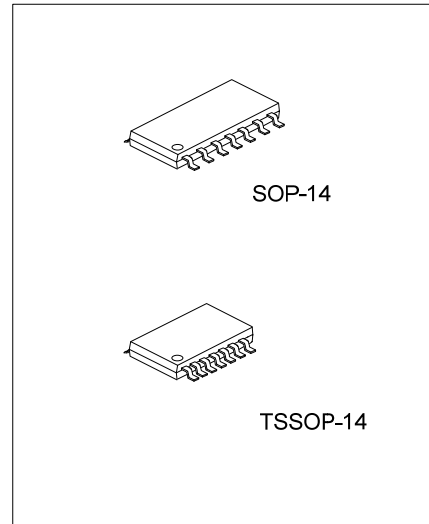
DESCRIPTION

The **U74AHC125** is a quadruple bus buffer gate with 3-state output. When \overline{OE} is high, the Y output is in a high-impedance state. When \overline{OE} is low, the device passes non inverted data from the A input to the Y output.

To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to V_{CC} through a pull-up resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

FEATURES

- * Wide supply voltage range from 2V to 5.5V
- * Max t_{PD} of 5.5ns from A to Y at 5V, $C_L=15pF$
- * Low Quiescent Current: $I_{CC} = 4 \mu A$ (Max.) at 5.5V
- * ± 8 mA Output Driver at 5V

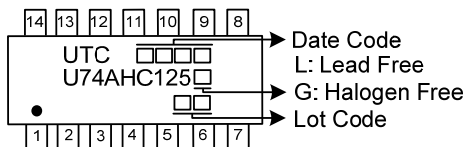


ORDERING INFORMATION

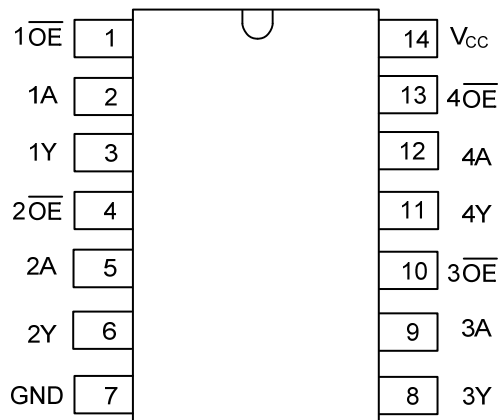
Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AHC125L-S14-R	U74AHC125G-S14-R	SOP-14	Tape Reel
U74AHC125L-P14-R	U74AHC125G-P14-R	TSSOP-14	Tape Reel

<p>U74AHC125G-S14-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) S14: SOP-14, P14: TSSOP-14</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



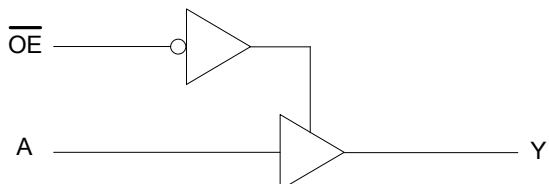
■ PIN CONFIGURATION



■ FUNCTION TABLE

INPUTS		OUTPUTS
\overline{OE}	A	Y
L	H	H
L	L	L
H	X	Z

■ LOGIC DIAGRAM



■ ABSOLUTE MAXIMUM RATING (unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{CC}	-0.5~7	V
Input Voltage	V_{IN}	-0.5~7	V
Output Voltage	V_{OUT}	-0.5~ V_{CC} +0.5	V
Input Clamp Current ($V_{IN}<0$)	I_{IK}	-20	mA
Output Clamp Current ($V_{OUT}<0$, or $V_{OUT}>V_{CC}$)	I_{OK}	± 20	mA
Output Current	I_{OUT}	± 25	mA
V_{CC} or GND Current	I_{CC}	± 50	mA
Storage Temperature	T_{STG}	-65 ~ +150	$^{\circ}C$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{CC}		2		5.5	V
High-Level Input Voltage	V_{IH}	$V_{CC}=2V$	1.5			V
		$V_{CC}=3V$	2.1			
		$V_{CC}=5.5V$	3.85			
Low-Level Input Voltage	V_{IL}	$V_{CC}=2V$			0.5	V
		$V_{CC}=3V$			0.9	
		$V_{CC}=5.5V$			1.65	
Input Voltage	V_{IN}		0		5.5	V
Output Voltage	V_{OUT}		0		V_{CC}	V
High-level Output Current	I_{OH}	$V_{CC}=2V$			-50	μA
		$V_{CC}=3.3\pm 0.3V$			-4	mA
		$V_{CC}=5\pm 0.5V$			-8	
Low-level Output Current	I_{OL}	$V_{CC}=2V$			50	μA
		$V_{CC}=3.3\pm 0.3V$			4	mA
		$V_{CC}=5\pm 0.5V$			8	
Input Transition Rise or Fall Rate	$\Delta t/\Delta V$	$V_{CC}=3.3\pm 0.3V$			100	ns/V
		$V_{CC}=5\pm 0.5V$			20	
Operating Temperature	T_A		-40		85	$^{\circ}C$

■ ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-Level Output Voltage	V_{OH}	$I_{OH}=-50\mu A, V_{CC}=2V$	1.9	2		V
		$I_{OH}=-50\mu A, V_{CC}=3V$	2.9	3		
		$I_{OH}=-50\mu A, V_{CC}=4.5V$	4.4	4.5		
		$I_{OH}=-4mA, V_{CC}=3V$	2.58			
		$I_{OH}=-8mA, V_{CC}=4.5V$	3.94			
Low-Level Output Voltage	V_{OL}	$I_{OL}=50\mu A, V_{CC}=2V$			0.1	V
		$I_{OL}=50\mu A, V_{CC}=3V$			0.1	
		$I_{OL}=50\mu A, V_{CC}=4.5V$			0.1	
		$I_{OL}=4mA, V_{CC}=3V$			0.36	
		$I_{OL}=8mA, V_{CC}=4.5V$			0.36	
Input Leakage Current (A or \overline{OE} Input)	$I_{I(LEAK)}$	$V_{IN}=5.5V$ or GND, $V_{CC}=0\sim 5.5V$			± 0.1	μA
High-impedance State Current	I_{OZ}	$V_{OUT}=V_{CC}$ or GND, $V_{CC}=5.5V$			± 0.25	μA
Quiescent Supply Current	I_{CC}	$V_{IN}=V_{CC}$ or GND, $I_{OUT}=0$, $V_{CC}=5.5V$			4	μA
Input Capacitance	C_{IN}	$V_{IN}=V_{CC}$ or GND, $V_{CC}=5V$		4	10	pF

■ SWITCHING CHARACTERISTICS

($V_{CC} = 3.3V \pm 0.3V$, $T_A = 25^\circ C$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation Delay from Input A to Output Y, t_{PD}	t_{PLH}	$C_L=15pF, R_L=1k\Omega$		5.6	8	ns
		$C_L=50pF, R_L=1k\Omega$		8.1	11.5	
	t_{PHL}	$C_L=15pF, R_L=1k\Omega$		5.6	8	ns
		$C_L=50pF, R_L=1k\Omega$		8.1	11.5	
Propagation Delay from Input \overline{OE} to Output Y, t_{EN}	t_{PZH}	$C_L=15pF, R_L=1k\Omega$		5.4	8	ns
		$C_L=50pF, R_L=1k\Omega$		7.9	11.5	
	t_{PZL}	$C_L=15pF, R_L=1k\Omega$		5.4	8	ns
		$C_L=50pF, R_L=1k\Omega$		7.9	11.5	
Propagation Delay from Input \overline{OE} to Output Y, t_{DIS}	t_{PHZ}	$C_L=15pF, R_L=1k\Omega$		7	9.7	ns
		$C_L=50pF, R_L=1k\Omega$		9.5	13.2	
	t_{PLZ}	$C_L=15pF, R_L=1k\Omega$		7	9.7	ns
		$C_L=50pF, R_L=1k\Omega$		9.5	13.2	

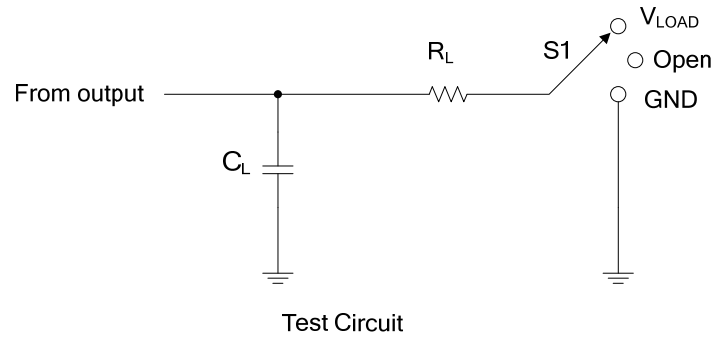
($V_{CC} = 5V \pm 0.5V$, $T_A = 25^\circ C$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation Delay from Input A to Output Y, t_{PD}	t_{PLH}	$C_L=15pF, R_L=1k\Omega$		3.8	5.5	ns
		$C_L=50pF, R_L=1k\Omega$		5.3	7.5	
	t_{PHL}	$C_L=15pF, R_L=1k\Omega$		3.8	5.5	ns
		$C_L=50pF, R_L=1k\Omega$		5.3	7.5	
Propagation Delay from Input \overline{OE} to Output Y, t_{EN}	t_{PZH}	$C_L=15pF, R_L=1k\Omega$		3.6	5.1	ns
		$C_L=50pF, R_L=1k\Omega$		5.1	7.1	
	t_{PZL}	$C_L=15pF, R_L=1k\Omega$		3.6	5.1	ns
		$C_L=50pF, R_L=1k\Omega$		5.1	7.1	
Propagation Delay from Input \overline{OE} to Output Y, t_{DIS}	t_{PHZ}	$C_L=15pF, R_L=1k\Omega$		4.6	6.8	ns
		$C_L=50pF, R_L=1k\Omega$		6.1	8.8	
	t_{PLZ}	$C_L=15pF, R_L=1k\Omega$		4.6	6.8	ns
		$C_L=50pF, R_L=1k\Omega$		6.1	8.8	

■ OPERATING CHARACTERISTICS ($T_A = 25^\circ C$, unless otherwise specified)

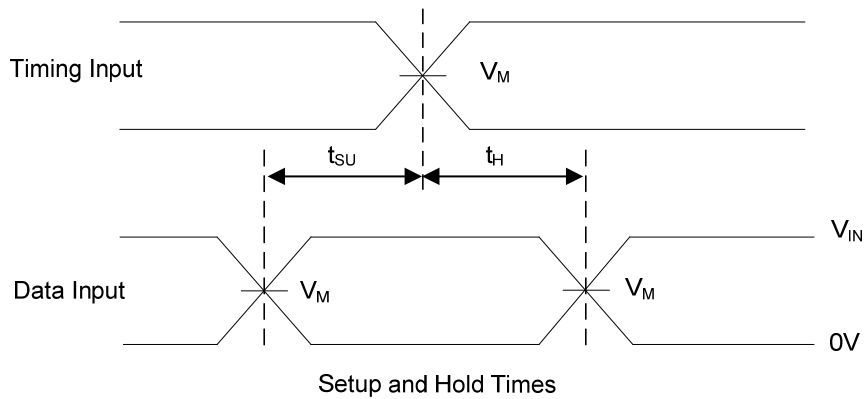
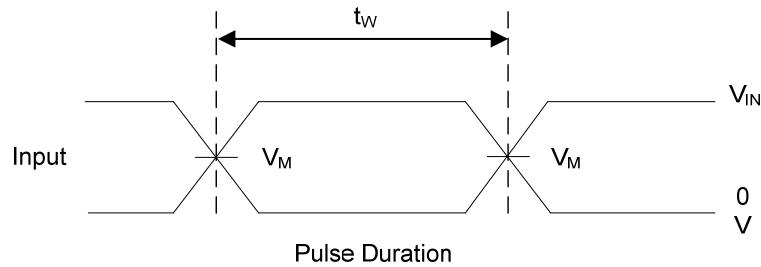
PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Power Dissipation Capacitance	C_{PD}	$V_{CC} = 5V, f=1MHz, No\ load$	14	pF

■ TEST CIRCUIT AND WAVEFORMS

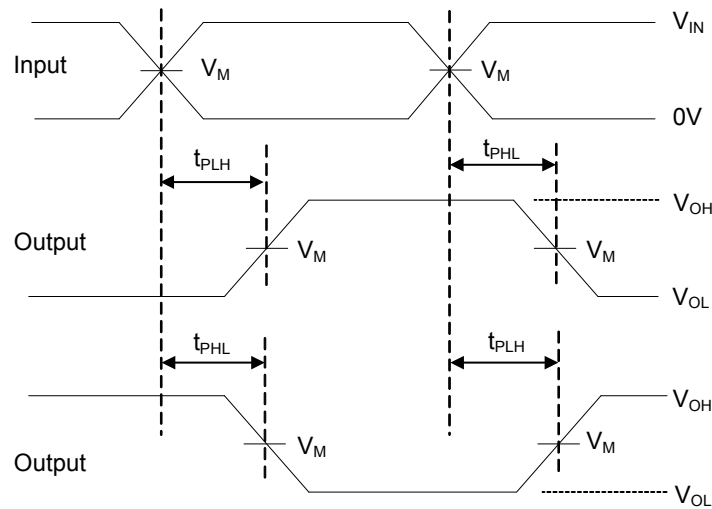


TEST	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

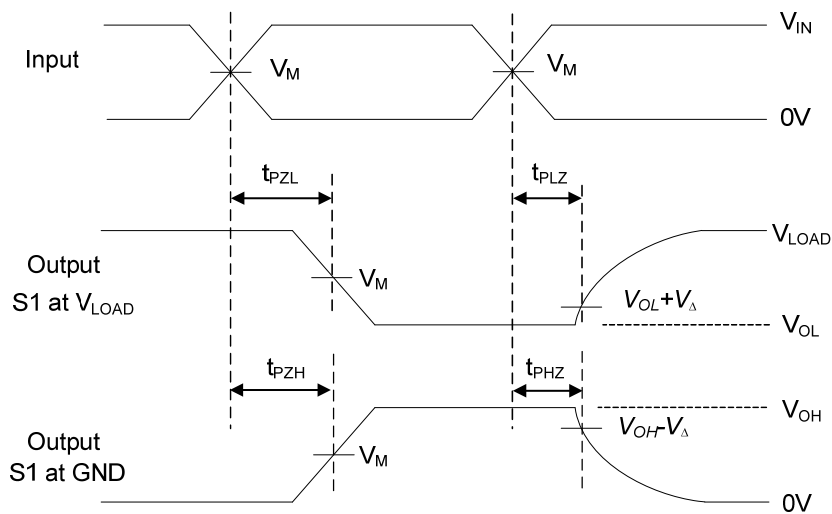
V_{CC}	Input		V_M	V_{LOAD}	C_L	R_L	V_{Δ}
	V_{IN}	t_r, t_f					
$3.3V \pm 0.3V$	V_{CC}	$\leq 3ns$	$V_{CC}/2$	V_{CC}	15pF	1k Ω	0.3V
					50pF		
$5V \pm 0.5V$	V_{CC}	$\leq 3ns$	$V_{CC}/2$	V_{CC}	15pF	1k Ω	0.5V
					50pF		



■ TEST CIRCUIT AND WAVEFORMS (Cont.)



Voltage Waveforms Propagation Delay Times



Voltage Waveforms Enable and Disable Times

Notes: 1. C_L includes probe and jig capacitance.

2. All input pulses are supplied by generators having the following characteristics: PRR \leq 1MHz, $Z_O = 50\Omega$.

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