



## U74AHCT1G66

CMOS IC

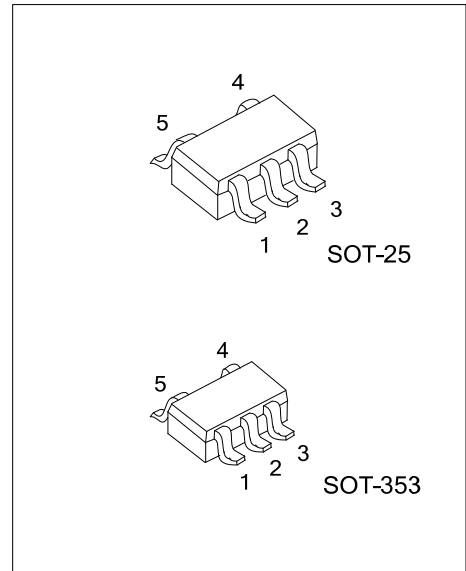
### BILATERAL SWITCH

#### DESCRIPTION

The **U74AHCT1G66** is an analog switch which transmits signals from pin(Y or Z) to pin (Z or Y) with an active HIGH enable input pin (E). When pin E is LOW, the switch is turned off.

#### FEATURES

- \* Operation voltage range: 4.5~5.5V
- \* Low power dissipation
- \* Very low ON-resistance: 26Ω (typ.) at V<sub>CC</sub>=3.0V  
16Ω (typ.) at V<sub>CC</sub>=4.5V  
14Ω (typ.) at V<sub>CC</sub>=5.5V
- \* TTL-Voltage Compatible

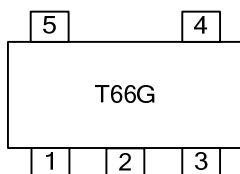


#### ORDERING INFORMATION

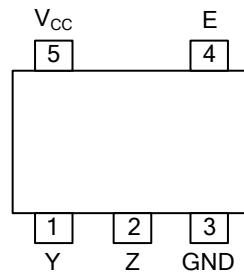
Ordering Number	Package	Packing
U74AHCT1G66G-AF5-R	SOT-25	Tape Reel
U74AHCT1G66G-AL5-R	SOT-353	Tape Reel

<p>U74AHCT1G66G-AF5-R</p>	<p>(1) R: Tape Reel (2) AF5: SOT-25, AL5: SOT-353 (3) G: Halogen Free and Lead Free</p>
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#### MARKING



■ PIN CONFIGURATION



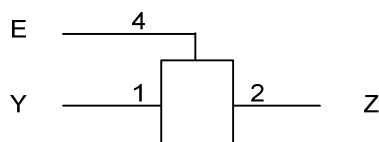
■ PIN DESCRIPTION

PIN NO.	SYMBOL	DESCRIPTION
1	Y	independent input/output
2	Z	independent output/input
3	GND	ground
4	E	enable input
5	V <sub>CC</sub>	supply voltage

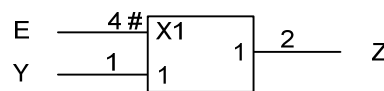
■ FUNCTION TABLE (each gate)

INPUT E	SWITCH
H	ON
L	OFF

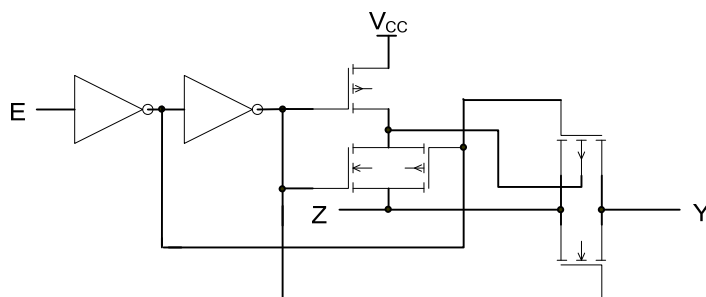
■ LOGIC DIAGRAM (positive logic)



Logic symbol



IEC logic symbol



## ■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{CC}$	-0.5~7	V
Enable Input Voltage	$V_E$	-0.5~7	V
Enable Input Clamp Current	$I_{EK}$	-20	mA
Switch Diode Current	$I_{SK}$	$\pm 20$	mA
On-State Switch current (-0.5V< $V_{OS}$ < $V_{CC}+0.5V$ )	$I_S$	$\pm 25$	mA
$V_{CC}$ or GND Current	$I_{CC}$	$\pm 75$	mA
Power Dissipation	$P_D$	250	mW
Storage Temperature	$T_{STG}$	-65 ~ +150	°C

- Notes: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
 2. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.  
 3. To avoid drawing  $V_{CC}$  current out of pin Z, when switch current flows into pin Y, the voltage drop across the bidirectional switch must not exceed 0.4V. If the switch current flows into pin Z, no  $V_{CC}$  current will flow out of pin Y. In this case there is no limit for the voltage drop across the switch, but the voltage at pins Y and Z may not exceed  $V_{CC}$  or GND.

## ■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	$V_{CC}$		4.5	5.0	5.5	V
Enable input Voltage	$V_E$		0		5.5	V
Switch Voltage	$V_S$		0		$V_{CC}$	V
Input Transition Rise or Fall Rate	$t_R, t_F$	$V_{CC}=3.3+0.3V$				ns/V
		$V_{CC}=5.0+0.5V$			20	
Operating Temperature	$T_A$		-40	25	125	°C

## ■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Input Voltage	High-Level	$V_{IH}$	$V_{CC}=4.5V\sim 5.5V$	2.0		V	
	Low-Level	$V_{IL}$	$V_{CC}=4.5V\sim 5.5V$			0.8	
Enable Input Leakage Current	$I_{E(LEAK)}$	$V_{CC}=5.5V, V_E = V_{CC}$ or GND			0.1	$\mu A$	
Switch Current	OFF-State	$I_S$	$V_{CC}=5.5V,  V_S =V_{CC} - GND$			0.1	$\mu A$
	ON-State			$V_{CC}=5.5V$			0.1
ON-Resistance(Peak)	$R_{ON(PEAK)}$	$V_{CC}=4.5V\sim 5.5V, V_{IS} = V_{CC}$ to GND, $I_S=10mA$		15	30	$\Omega$	
ON-Resistance(Rail)	$R_{ON(RAIL)}$	$V_{CC}=4.5V\sim 5.5V, V_{IS}= V_{CC}, I_S=10mA$		13	22	$\Omega$	
		$V_{CC}=4.5V\sim 5.5V, V_{IS}= GND, I_S=10mA$		15	22	$\Omega$	
Quiescent Supply Current	$I_Q$	$V_{CC}=5.5V, V_E = V_{CC}$ or GND, $V_{IS}= GND$ or $V_{CC}, V_{OS}=V_{CC}$ or GND			1.0	$\mu A$	
Additional Quiescent Supply Current	$\Delta I_Q$	$V_{CC}=5.5V, V_E = 3.4V$ , Other inputs at $V_{CC}$ or GND, $I_{OUT}=0$			1.35	mA	
Enable Input Capacitance	$C_E$	$V_E=V_{CC}$ or GND		2	10	pF	
Maximum Switch Capacitance	$C_S$	Independent I/O		4	10	pF	

■ DYNAMIC CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

(Input: t<sub>R</sub>, t<sub>F</sub>≤3ns; PRR≤1MHz, All typical values are measured at V<sub>CC</sub>=5V.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation Delay From Y/Z TO Z/Y	t <sub>PHL</sub> /t <sub>PLH</sub>	V <sub>CC</sub> =4.5V~5.5V, C <sub>L</sub> =50pF		0.7	1	ns
Turn-On Time From E TO Z/Y	t <sub>PZH</sub> /t <sub>PZL</sub>	V <sub>CC</sub> =4.5V~5.5V, C <sub>L</sub> =15pF		3	7	ns
		V <sub>CC</sub> =4.5V~5.5V, C <sub>L</sub> =50pF		4.7	10	ns
Turn-Off Time From E TO Z/Y	t <sub>PHZ</sub> /t <sub>PLZ</sub>	V <sub>CC</sub> =4.5V~5.5V, C <sub>L</sub> =15pF		5	8	ns
		V <sub>CC</sub> =4.5V~5.5V, C <sub>L</sub> =50pF		6.5	11	ns

■ RECOMMENDED CONDITIONS AND TYPICAL VALUES (GND=0; t<sub>R</sub>=t<sub>F</sub>=3ns)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Sine-Wave Distortion at f=1kHz		R <sub>L</sub> =10kΩ, C <sub>L</sub> =50pF	V <sub>CC</sub> =3.0V~3.6V, V <sub>IS(P-P)</sub> =2.5V		0.025	V
			V <sub>CC</sub> =4.5V~5.5V, V <sub>IS(P-P)</sub> =4.0V		0.015	
Sine-Wave Distortion at f=10kHz		R <sub>L</sub> =10kΩ, C <sub>L</sub> =50pF	V <sub>CC</sub> =3.0V~3.6V, V <sub>IS(P-P)</sub> =2.5V		0.025	V
			V <sub>CC</sub> =4.5V~5.5V, V <sub>IS(P-P)</sub> =4.0V		0.015	
Switch OFF Signal Feed-Through (Note 4)		R <sub>L</sub> =600Ω, C <sub>L</sub> =50pF	V <sub>CC</sub> =3.0V~3.6V, F=1MHz		-50	V
			V <sub>CC</sub> =4.5V~5.5V, F=1MHz		-50	
Minimum Frequency Response(-3dB) (Note 5)	f <sub>MAX</sub>	R <sub>L</sub> =50Ω, C <sub>L</sub> =10pF	V <sub>CC</sub> =3.0V~3.6V		230	ns/V
			V <sub>CC</sub> =4.5V~5.5V		280	

**OPERATING CHARACTERISTICS**

Power Dissipation Capacitance	C <sub>PD</sub>	V <sub>CC</sub> =5, C <sub>L</sub> =50pF, f=10MHz		15		pF
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Notes: 4. Adjust input voltage V<sub>IS</sub> is 0dbm level (0dbm=1mW into 600Ω)

5. Adjust input voltage V<sub>IS</sub> is 0dbm level at V<sub>OS</sub> for 1MHz (0dbm=1mW into 50Ω)

■ TEST CIRCUIT AND WAVEFORMS

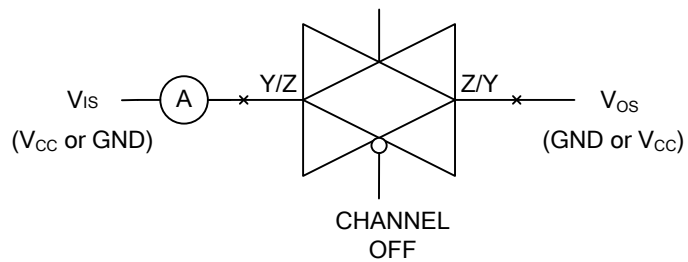


Fig. 1 OFF-State Switch Leakage Current Test Circuit

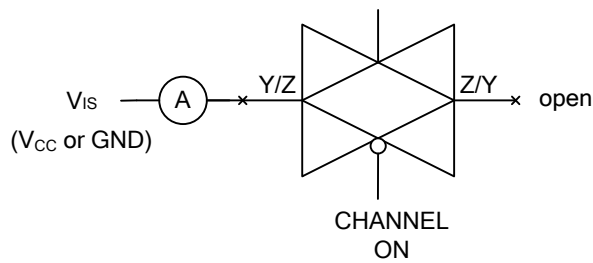


Fig. 2 ON-State Leakage Current Test Circuit

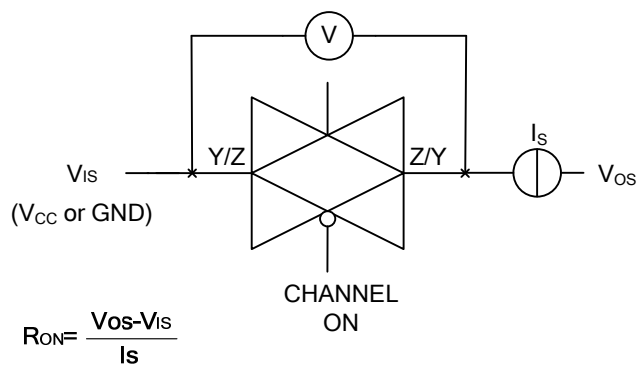


Fig. 3 ON-State Resistance Test Circuit

■ TEST CIRCUIT AND WAVEFORMS(Cont.)

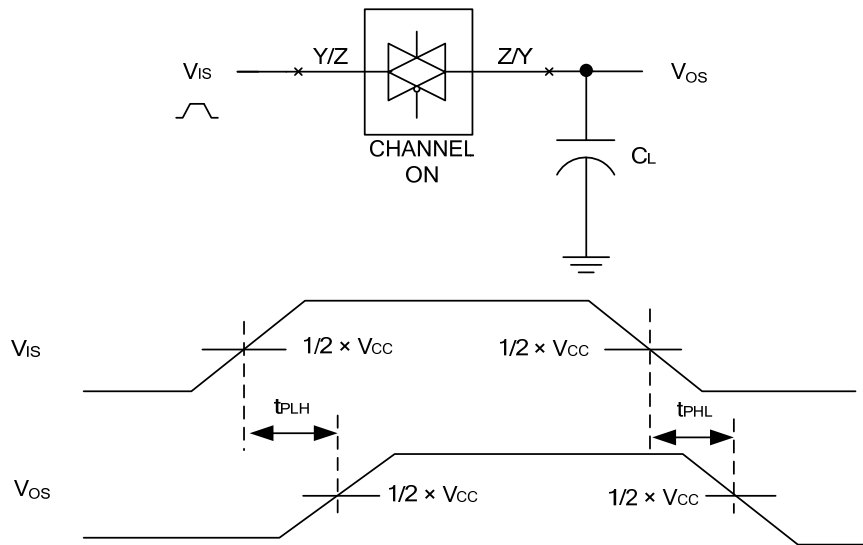


Fig. 4 The input (Y/Z) to output (Z/Y) propagation delays.

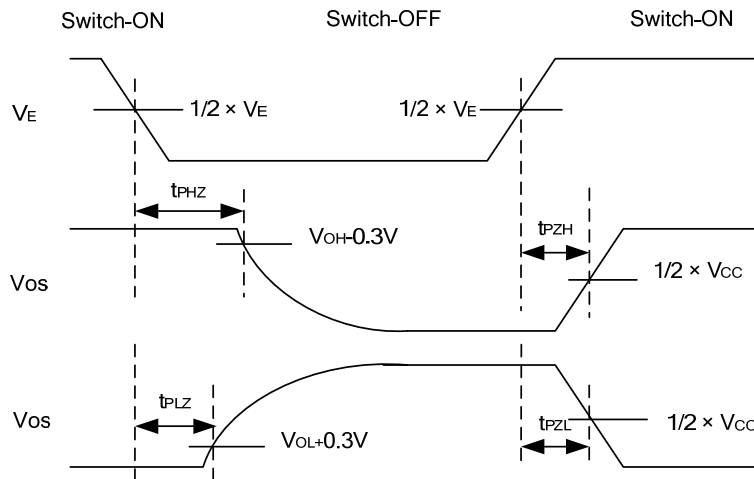
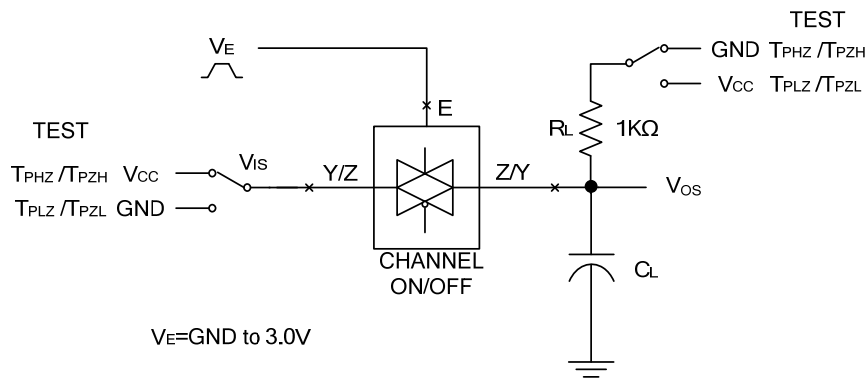


Fig. 5 The switch-on and switch-off times.

■ TEST CIRCUIT AND WAVEFORMS(Cont.)

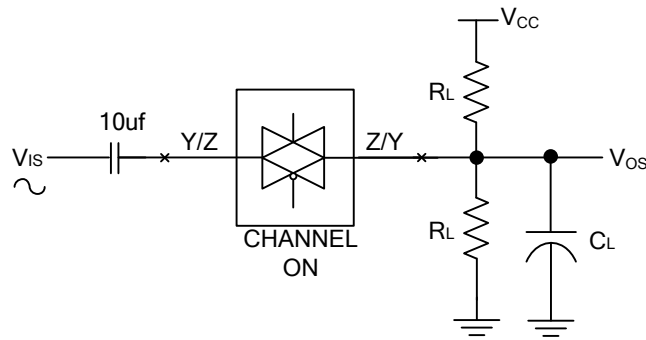


Fig. 6 Sine-Wave Distortion

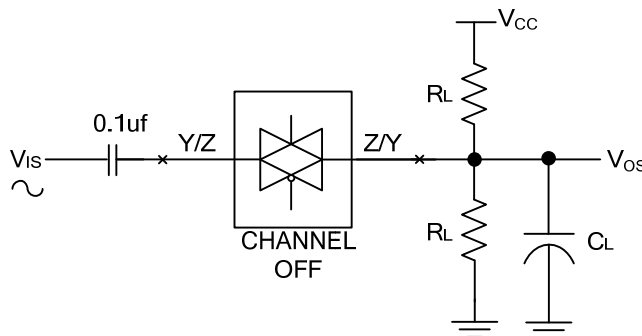


Fig. 7 Feed-through Attenuation (Switch OFF)

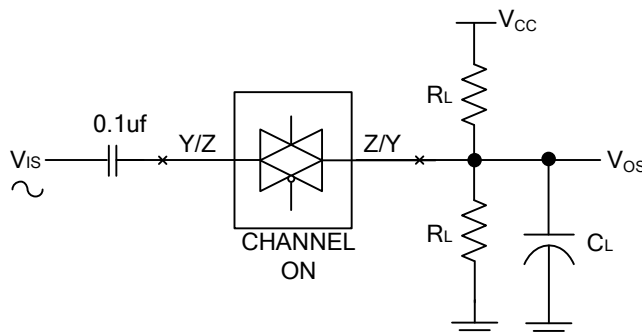


Fig. 8 Minimum Frequency Response

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