



UD06123

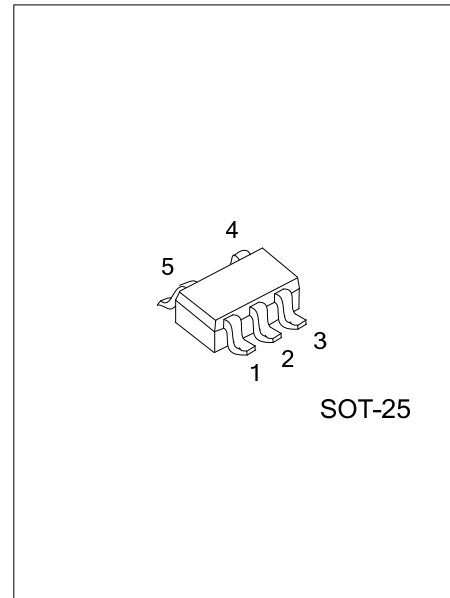
Preliminary

CMOS IC

2.25MHz, 1.2A, $V_{FB}=0.6V$ SYNCHRONOUS STEP-DOWN CONVERTER

DESCRIPTION

UTC **UD06123** is a 1.4MHz constant frequency current mode PWM step-down converter. It is ideal for portable equipment requiring very high current up to 1.8A from single-cell Lithium-ion batteries while still achieving over 90% efficiency during peak load conditions. The 2.5V to 7.5V input voltage range makes UTC **UD06123** ideally suited for single Li-Ion, two to four AA battery-powered applications. 100% duty cycle provides low dropout operation, extending battery life in portable systems. Pulse skipping mode operation provides very low output ripple voltage for noise sensitive applications. Switching frequency is internally set at 2.25MHz, allowing the use of small surface mount inductors and capacitors. The internal synchronous switch increases efficiency and decreases need of an external Schottky diode. Low output voltages are easily supported with the 0.6V feedback reference voltage.



FEATURES

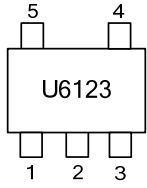
- * Input Voltage Range :2.5V~7.5V
- * Output Voltage: from 0.6V to V_{IN}
- * High Efficiency: Up to 92%
- * Constant Frequency Operation:2.3MHz
- * Output Current:1.2A
- * Quiescent Current: 50uA (input < 4.2V)
- * No Schottky Diode Required
- * 100% Duty Cycle in Dropout
- * 0.6V Reference Allows Low Output Voltages
- * Current Mode Operation for Excellent Line and Load Transient Response
- * Current limit, Enable function
- * Short Circuit Protect (SCP)
- * $\leq 1\mu A$ Shutdown Current

ORDERING INFORMATION

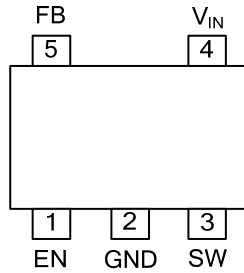
Ordering Number	Package	Packing
UD06123G-AF5-R	SOT-25	Tape Reel

<p>UD06123G-AF5-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) AF5: SOT-25</p> <p>(3) G: Halogen Free and Lead Free</p>
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MARKING



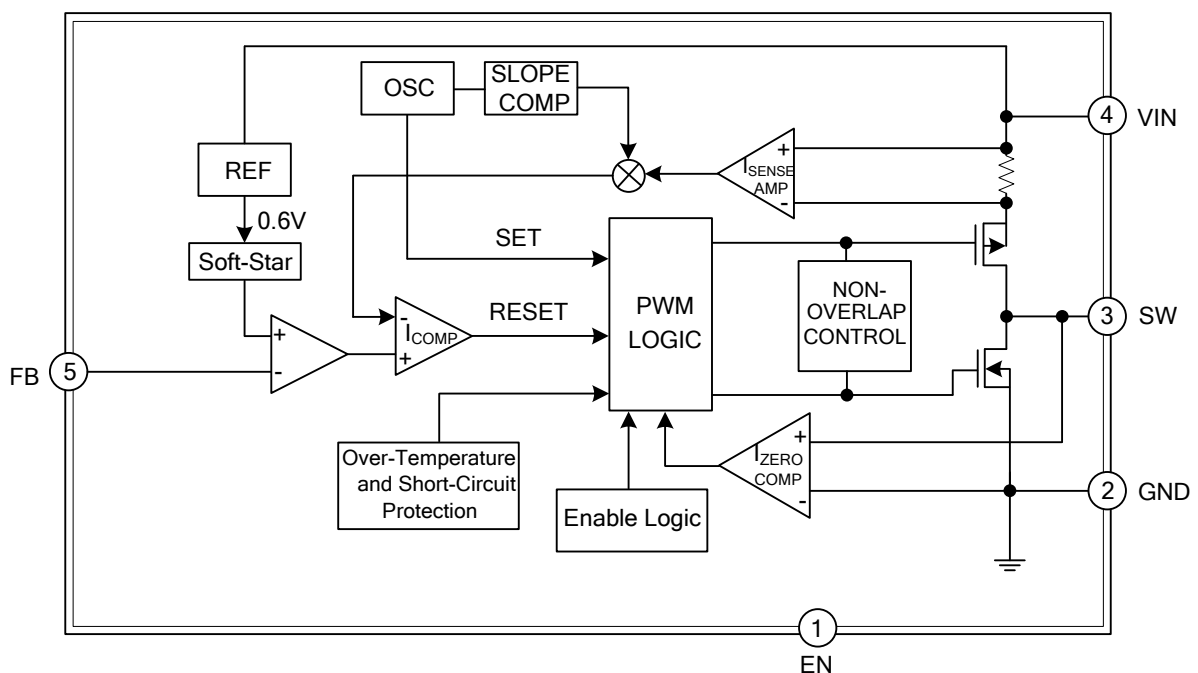
PIN CONFIGURATION



PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	EN	Enable pin H: Normal operation ; L: Shutdown
2	GND	Ground Pin
3	SW	Switch output pin. Connect external inductor here. Minimize trace area at this pin to reduce EMI.
4	V _{IN}	Power Supply Input Pin
5	FB	Output Feedback pin

BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
V_{IN} Pin Voltage	V_{IN}	8	V
Feedback Pin Voltage	V_{FB}	$V_{CC}+0.3$	V
EN Pin Voltage	V_{EN}	$V_{CC}+0.3$	V
Switch Pin Voltage	V_{SW}	$V_{CC}+0.3$	V
Peak SW Sink and Source Current	I_{PSW}	1.5	A
Junction Temperature	T_J	125	$^{\circ}\text{C}$
Operation Temperature Range	T_{OPR}	-40 ~ +85	$^{\circ}\text{C}$
Storage Temperature Range	T_{STG}	-40 ~ +150	$^{\circ}\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	280	$^{\circ}\text{C}/\text{W}$

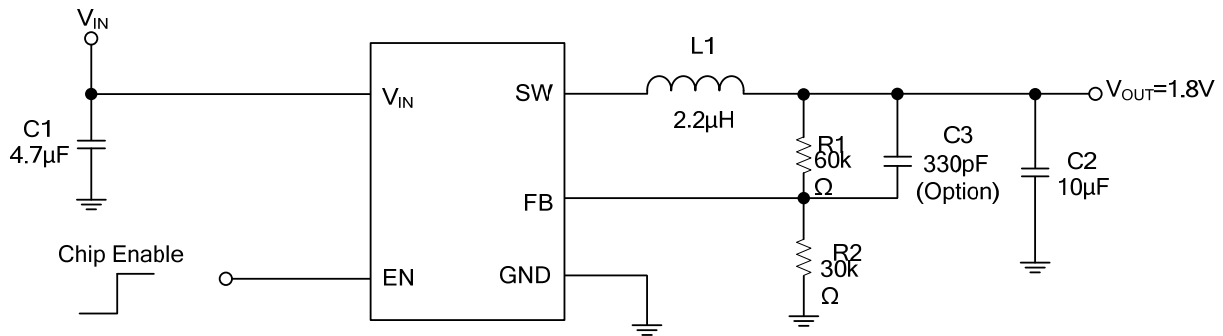
■ ELECTRICAL CHARACTERISTICS ($V_{IN}=V_{EN}=3.6\text{V}$, $T_A=25^{\circ}\text{C}$, unless otherwise specified) (Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage Range (Note 2)	V_{IN}		2.5		7.5	V
Under Voltage Lock Out Threshold	V_{UVLO}	V_{IN} Rising	1.8	2.1	2.3	V
		Hysteresis	100	250	400	mV
Feedback Voltage	V_{FB}	$V_{FB}=0.6\text{V}$	0.5880	0.6000	0.6120	V
Feedback Bias Current	I_{FB}	$V_{FB}=0.65\text{V}$	-30		+30	nA
Quiescent Current	I_{CC}	$V_{FB}=1\text{V}$		50	90	μA
Shutdown Supply Current	I_{SD}	$V_{EN}=0\text{V}$		0.1	1	μA
Switching Current Limit	I_{LIMIT}	$V_{IN}=5\text{V}$		1.8		A
Line Regulation	$\Delta V_{OUT}/V_{OUT}$	$V_{IN}=2.5\text{V}\sim 5.5\text{V}$		0.04	0.4	%/V
Load Regulation	$\Delta V_{OUT}/V_{OUT}$	$I_{OUT}=0.01\sim 1.2\text{A}$		0.5	0.6	%
Oscillation Frequency	F_{OSC}	SW pin		2.3		MHz
$R_{DS(ON)}$ of P-CH MOSFET	$R_{DS(ON)}$	$I_{OUT}=1.0\text{A}$		0.26	0.33	Ω
$R_{DS(ON)}$ of N-CH MOSFET	$R_{DS(ON)}$	$I_{OUT}=1.0\text{A}$		0.26	0.33	Ω
Efficiency	EFFI	$V_{IN}=5\text{V}$, $V_{OUT}=3.3\text{V}$, $I_{OUT}=0.5\text{A}$		92		%
EN Pin Logic Input Threshold Voltage	V_{ENL}				0.6	V
	V_{ENH}		1.2			V
EN Pin Input Current	I_{EN}			± 0.1	± 1	μA
Soft-Start Interval	T_{SS}			1.2		ms

Notes: 1. 100% production test at $+25^{\circ}\text{C}$. Specifications over the temperature range are guaranteed by design and characterization.

2. $V_{IN}(\text{Min.}) > V_{OUT}(\text{Max.}) + I_O(\text{Max.}) \cdot (P\text{-CH MOSFET } R_{DS(ON)} + \text{LDCR})$

■ TYPICAL APPLICATION CIRCUIT



$$V_{OUT} = 0.6 \times (1 + R1/R2)$$

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