



## UPSRB03

Preliminary

LINEAR INTEGRATED CIRCUIT

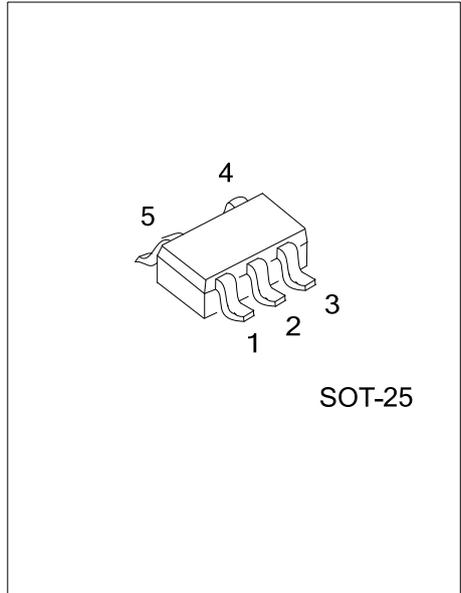
### HIGH PRECISION CC/CV PRIMARY SIDE SWITCHING REGULATOR

#### DESCRIPTION

The UTC **UPSRB03** is a primary control unit for switch mode charger and adapter applications. The controlled variable is transferred by an auxiliary winding from the secondary to the primary side. The device uses multi-mode operations to achieve high performance and high efficiency. Device operations in PFM in CC mode at large load condition and it operates in PWM with frequency reduction at light/medium load which can cancel audio noise.

The UTC **UPSRB03** operates in primary-side sensing and regulation. Opto-coupler and TL431 could be eliminated. It also eliminates the need of loop compensation circuitry while maintaining stability.

The UTC **UPSRB03** achieves high precision CV/CC regulation and high power efficiency. It offers comprehensive protection coverage with auto-recovery features including Cycle-by-cycle current limiting, output over voltage protection,  $V_{DD}$  over voltage protection, feedback loop open protection, short circuit protection, leading edge blanking,  $V_{DD}$  under voltage lockout, etc.



#### FEATURES

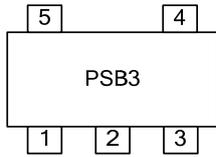
- \* Multi-Mode control for cancel audio noise
- \*  $\pm 5\%$  constant voltage regulation at universal AC input
- \* Primary side control without TL431 and opto-coupler
- \* High precision constant current regulation at universal AC input
- \* Programmable CV and CC regulation
- \* Eliminates control loop compensation circuitry
- \* Flyback topology in DCM operation
- \* Driver BJT switch
- \* Built-in Line CC compensation
- \* Built-in leading edge blanking
- \* Driver soft start
- \* Built-in primary winding inductance compensation
- \* Open circuit protection
- \* Output over voltage protection
- \*  $V_{DD}$  over voltage protection
- \* Short circuit protection

#### ORDERING INFORMATION

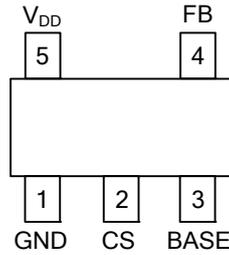
Ordering Number	Package	Packing
UPSRB03G-AF5-R	SOT25	Tape Reel

UPSRB03G-AF5-R	(1) Packing Type	(1) R: Tape Reel
	(2) Package Type	(2) AF5: SOT-25
	(3) Green Package	(3) G: Halogen Free and Lead Free

■ MARKING



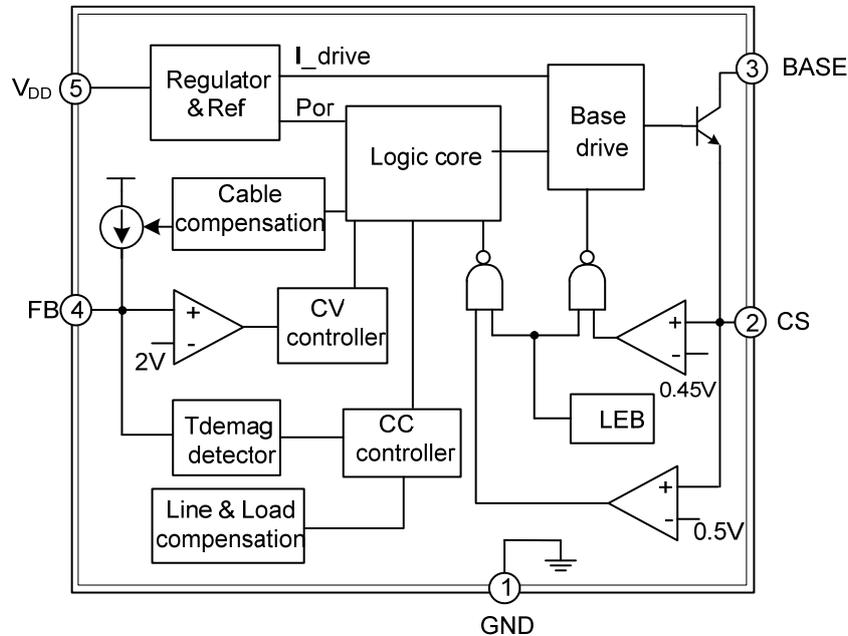
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	GND	Ground
2	CS	Current sense input.
3	BASE	Base drive for power BJT.
4	FB	The voltage feedback from the auxiliary winding
5	V <sub>DD</sub>	Power supply

■ BLOCK DIAGRAM



### ■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
V <sub>DD</sub> Voltage	V <sub>DD</sub>	-0.3~30	V
BASE Voltage	V <sub>BASE</sub>	-0.3~7	V
CS Input Voltage	V <sub>CS</sub>	-0.3~7	V
FB Input Voltage	V <sub>FB</sub>	-0.3~7	V
Junction Temperature	T <sub>J</sub>	-40 ~ +150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C
Lead Temperature (Soldering, 10 secs)	T <sub>OPR</sub>	260	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, V<sub>DD</sub>=20V, if not otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>Supply Voltage (V<sub>DD</sub>) Section</b>						
Start up Current	I <sub>ST</sub>	V <sub>DD</sub> = V <sub>TH (ON)</sub> -1V		3	20	μA
Static Current	I <sub>OP</sub>	V <sub>DD</sub> =20V		0.8	1.5	mA
Turn-off Threshold Voltage	V <sub>TH(OFF)</sub>		5.5	6.5	7.5	V
Turn-on Threshold Voltage	V <sub>TH (ON)</sub>		16.5	18	19.5	V
V <sub>DD</sub> Over Voltage Protection	V <sub>DD_OVP</sub>		25	27.5	30	V
V <sub>DD</sub> Clamp Voltage	V <sub>DD_Clamp</sub>		28	30	32	V
<b>Current Sense Input Section</b>						
LEB Time	T <sub>LEB</sub>			0.5		μS
Over Current Threshold	V <sub>CS</sub>		490	500	510	mV
OCP Propagation Delay	T <sub>D_OCP</sub>	From OCP Comparator to Base Drive		100		nS
<b>FB Input Section</b>						
Reference Voltage for Feedback Threshold	V <sub>FB</sub>		1.98	2.00	2.02	V
Demagnetization comparator threshold	V <sub>FB DEM</sub>			25		mV
Minimum Pause	T <sub>PAUSE MIN</sub>			2.0		μS
Maximum Pause	T <sub>PAUSE MAX</sub>			5		mS
Output Short Circuit Threshold	V <sub>FB short</sub>			0.6		V
Output Short Circuit Debounce Time	T <sub>FB short</sub>			13		mS
Ratio between switching period in CC mode and demagnetization time	T <sub>CC</sub> /T <sub>DEM</sub>			2		
Max Cable compensation current	I <sub>cable</sub>			60		μA
<b>BASE Drive Section</b>						
Base Sourcing Maximum Current	I <sub>OUT(SOURCE)</sub>		20	30	40	mA
Base Sourcing Current After Pre-off	I <sub>OUT(PRE)</sub>		0.5	1	1.5	mA
Base Drive Low Side On Resistor	R <sub>DSON L</sub>			1		Ω

### ■ OPERATION DESCRIPTION

The UTC **UPSRB03** is a primary control unit for switch mode charger and adapter applications. It operates in primary-side sensing and regulation. Opto-coupler and TL431 could be eliminated. Proprietary built-in CV and CC control can achieve high precision CV/CC performance. It uses Pulse Frequency Modulation (PFM) method to build discontinuous conduction mode (DCM) flyback power supplies.

#### Startup control

The  $V_{DD}$  pin of UTC **UPSRB03** is connected to the line input through a resistor. A large value startup resistor can be used to minimize the power loss in application because the start current of UTC **UPSRB03** is very low. When the  $V_{DD}$  voltage reaches  $V_{TH(ON)}$ , the internal startup circuit is disabled and the IC turns on.

#### Operating Current

The Operating current of UTC **UPSRB03** is as low as 800 $\mu$ A. Good efficiency and very low standby power can be achieved.

#### Constant Voltage Operation

The output voltage is defined by the transmission ratio between the secondary and auxiliary winding. The UTC **UPSRB03** captures the auxiliary winding feedback voltage at FB pin and operates in constant-voltage (CV) mode to regulate the output voltage. The auxiliary voltage reflects the output voltage is given by:

$$V_{AUX} = \frac{N_A}{N_S} \times (V_O + \Delta V) \quad (1)$$

Where  $\Delta V$  indicates the drop voltage of the output diode.

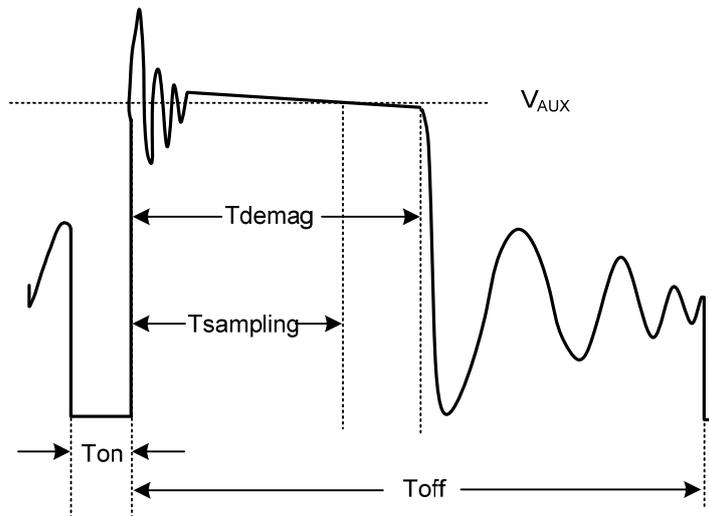


Figure2. Auxiliary voltage waveform

Via a resistor divider connected between the auxiliary winding and FB, the  $V_{AUX}$  is sampled at the  $T_{sampling}$  end and it is hold until the next sampling. The sampled voltage is compared with 2V reference voltage and the error is amplified. The error amplifier output reflects the load condition and controls the  $T_{off}$  time to regulate the output voltage, thus constant output voltage can be achieved.

#### Constant Current Operation

When the sampled voltage is below 2V reference voltage and the error amplifier output reaches its maximum, thus UTC **UPSRB03** operates in constant-current (CC) mode. The CC point and maximum output power can be externally adjusted by external current sense resistor  $R_{cs}$ . The larger  $R_{cs}$ , the smaller CC point is, and the smaller output power becomes.

■ OPERATION DESCRIPTION (Cont.)

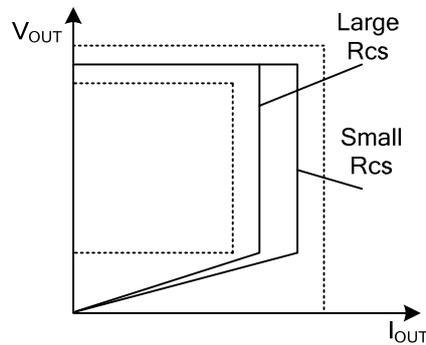


Figure3. Adjustable output power by changing Rcs

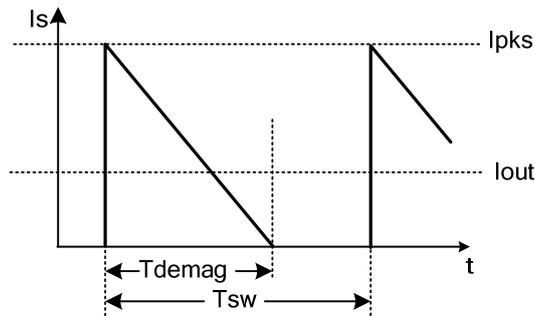


Figure4. Secondary current waveform

In CC operation, the CC loop control function of UTC **UPSRB03** will keep a fixed proportion between secondary inductance de-magnetization time ( $T_{demag}$ ) and switching cycle time ( $T_{sw}$ ). The fixed proportion is

$$\frac{T_{demag}}{T_{sw}} = \frac{1}{2} \tag{2}$$

Thus the output current is given by:

$$I_{out} = \frac{1}{2} \times \frac{N_p}{N_s} \times I_{PK} \times \frac{T_{demag}}{T_{sw}} = \frac{1}{4} \times \frac{N_p}{N_s} \times I_{PK} \tag{3}$$

**Current Sensing and Leading Edge Blanking**

Cycle-by-cycle current limiting is offered in UTC **UPSRB03**. The switch current is detected by a sense resistor into the CS pin. When the power switch is turned on, a turn-on spike will occur on this resistor. A 500ns leading-edge blanking is built in to avoid false-termination of the switching pulse so that the external RC filtering is no longer needed.

**Programmable Cable Drop Compensation**

UTC **UPSRB03** has a built-in cable voltage drop compensation to achieve good load regulation. An offset voltage is generated at FB pin by an internal current flowing into the resistor divider. The current is inversely proportional to the output load current. The voltage drop across the cable is compensated by this offset voltage at FB pin. It can also be programmed by adjusting the resistance of the divider to compensate the drop for various cable lines used.

**Built-in Load and Line CC Compensation**

In conventional PSR system, the output CC point can vary with output and line voltage. UTC **UPSRB03** has built-in blocks to compensate the variation. The device can adjust CC point based on sensed output voltage and PFM duty. In this way, CC accuracy can be improved.

■ OPERATION DESCRIPTION (Cont.)

**Drive BJT**

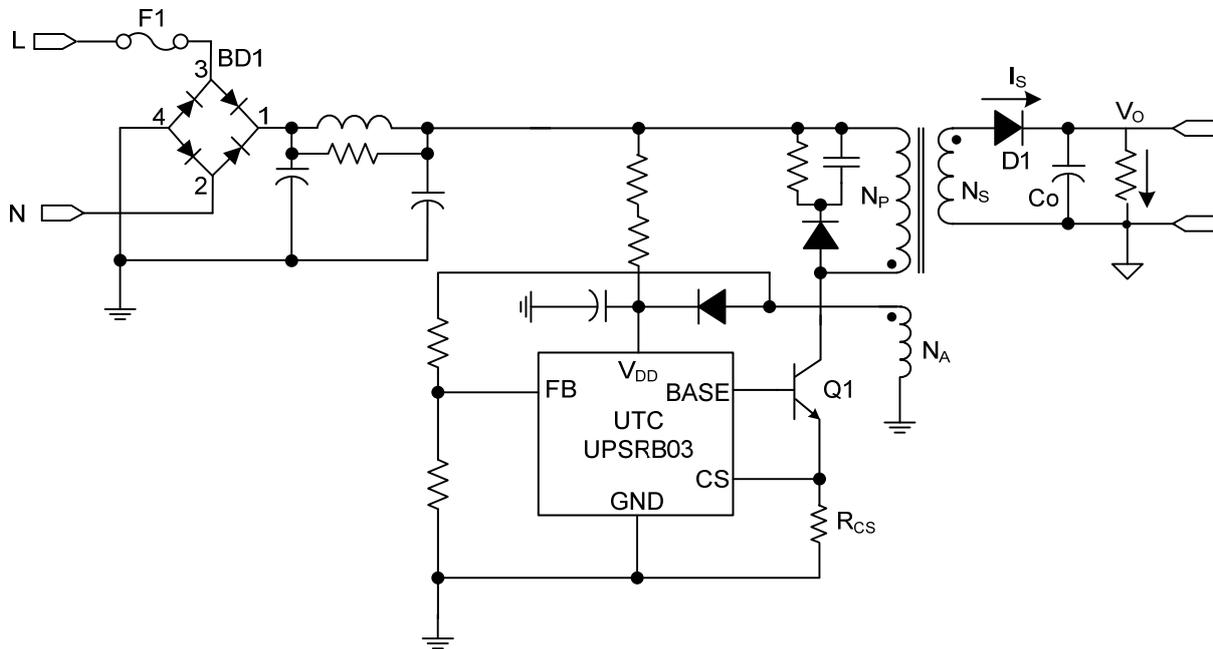
The driver is a push pull stage with the supply voltage  $V_{DD}$ . It provides the driving current for the external power bipolar transistor. The output signal is current-limited to  $I_{OUT(SOURCE)}$ .

**Protection Control**

Good power supply system reliability is achieved with its comprehensive protection features including output over-voltage protection,  $V_{DD}$  over voltage protection, feedback loop open protection, Cycle-by-cycle current limiting, short circuit protection, leading edge blanking and UVLO, etc.

$V_{DD}$  is supplied by transformer auxiliary winding output. The output of UTC **UPSRB03** is shutdown when  $V_{DD}$  drops below  $V_{TH(OFF)}$  and the power converter enters power on start-up sequence thereafter.

■ TYPICAL APPLICATION CIRCUIT



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