



## UT2340

Power MOSFET

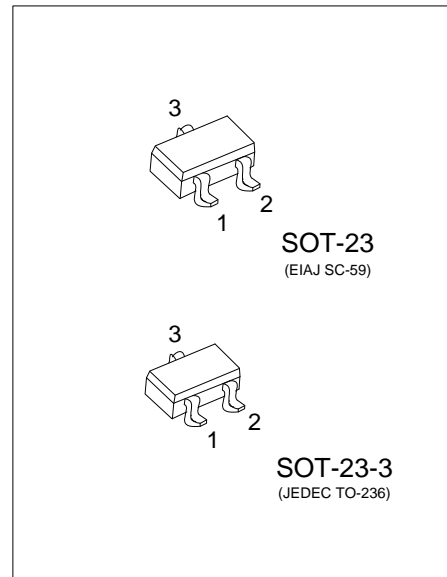
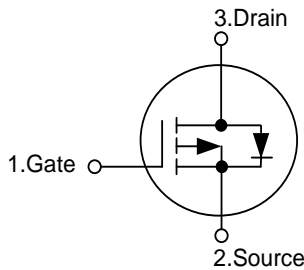
### -4.2A, -20V P-CHANNEL ENHANCEMENT MODE

#### DESCRIPTION

The UTC **UT2340** is P-Channel enhancement mode Power MOSFET, designed in serried ranks with fast switching speed, low on-resistance and favorable stabilization.

Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

#### SYMBOL



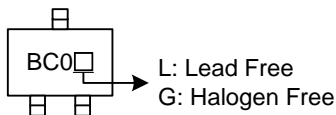
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT2340L-AE2-R	UT2340G-AE2-R	SOT-23-3	G	S	D	Tape Reel
UT2340L-AE3-R	UT2340G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>UT2340G-AE2-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) AE2: SOT-23-3, AE3: SOT-23</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATING	UNITS
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>GS</sub>	±8	V
Continuous Drain Current (Note 3) (T <sub>A</sub> =25°C)	I <sub>D</sub>	-4.2	A
Pulsed Drain Current (Note 1, 2)	I <sub>DM</sub>	-10	A
Power Dissipation	SOT-23-3	0.83	W
	SOT-23	1.38	W
Junction Temperature	T <sub>J</sub>	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ THERMAL DATA

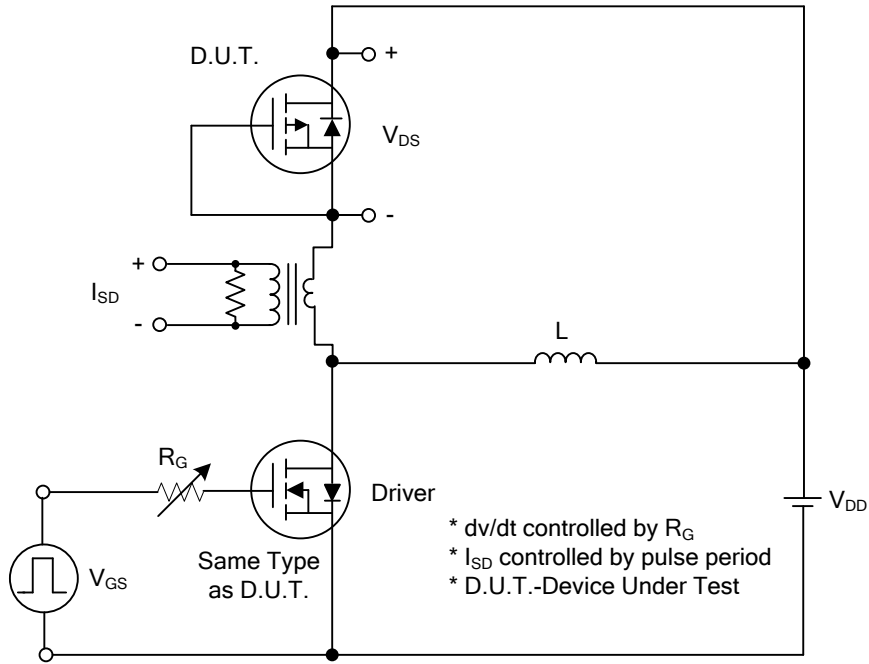
PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient (Note 3)	SOT-23-3	150	°C/W
	SOT-23	90	°C/W

### ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

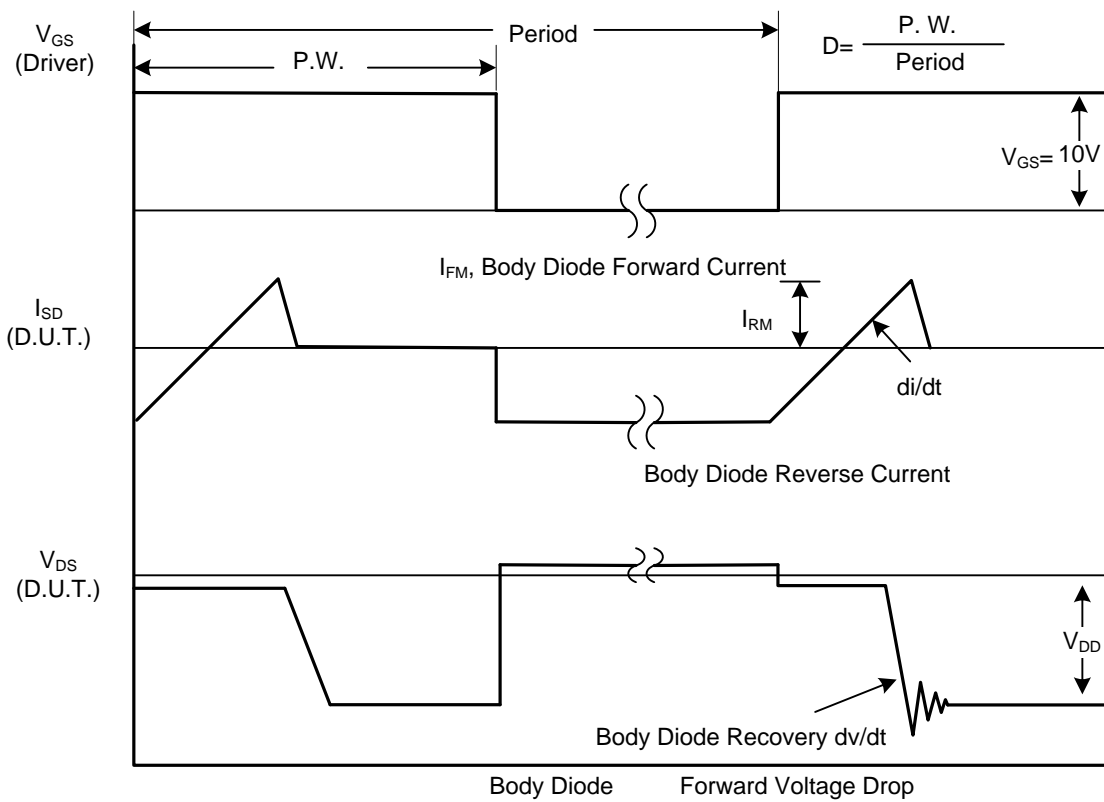
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-20			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V			-1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5		-1.2	V
Drain-Source On-State Resistance (Note 2)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.2A			70	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3.4A			110	mΩ
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-2.0A			210	mΩ
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-20V, f=1MHz		932		pF
Output Capacitance	C <sub>OSS</sub>			100		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			87		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 2)	Q <sub>G</sub>	V <sub>DS</sub> =-4V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.5A		12.5		nC
Gate-Source Charge	Q <sub>GS</sub>			2.5		nC
Gate-Drain Charge	Q <sub>GD</sub>			1.8		nC
Turn-ON Delay Time (Note 2)	t <sub>D(ON)</sub>	V <sub>DS</sub> =-4V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1A, R <sub>G</sub> =6Ω, R <sub>D</sub> =4Ω		10		ns
Turn-ON Rise Time	t <sub>R</sub>			39		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			42		ns
Turn-OFF Fall Time	t <sub>F</sub>			28		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>				-4.2	A
Maximum Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>				-10	A
Drain-Source Diode Forward Voltage(Note2)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1.2A			-1.2	V

- Notes: 1. Pulse width limited by T<sub>J(MAX)</sub>  
 2. Pulse width ≤300μs, duty cycle≤2%.  
 3. Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board; 270°C/W when mounted on min.

■ TEST CIRCUITS AND WAVEFORMS

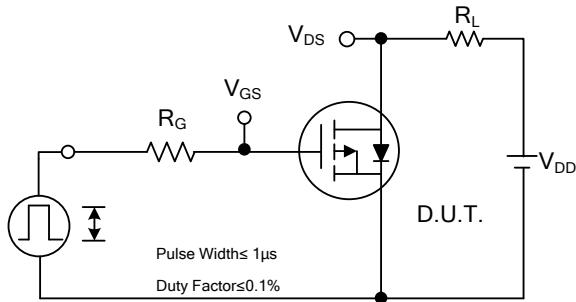


Peak Diode Recovery dv/dt Test Circuit

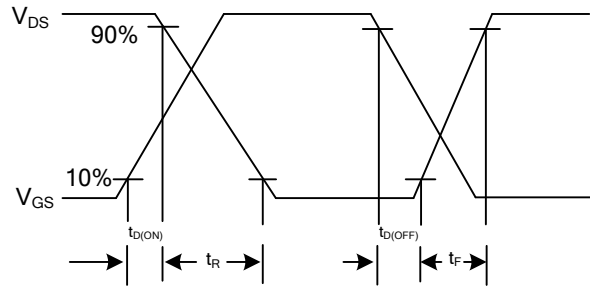


Peak Diode Recovery dv/dt Waveforms

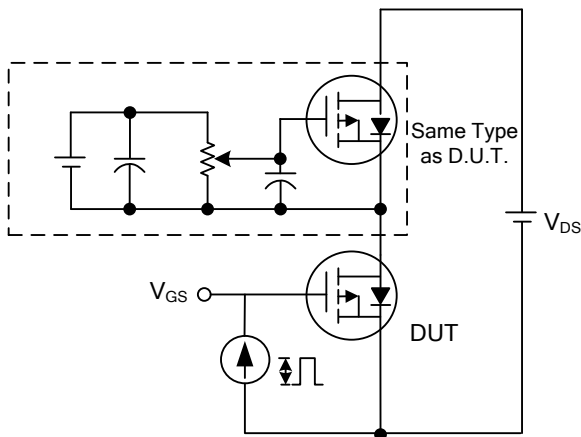
■ TEST CIRCUITS AND WAVEFORMS



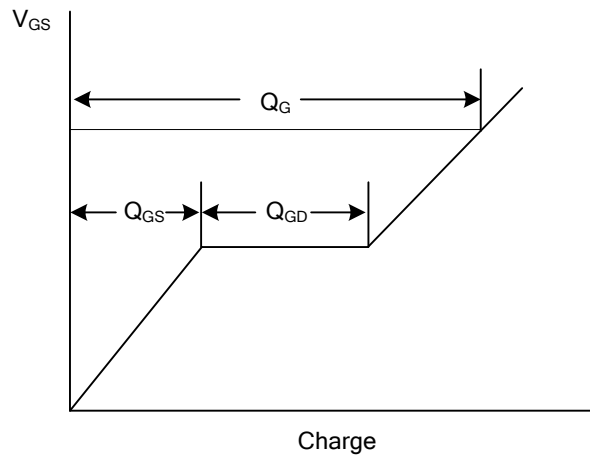
Switching Test Circuit



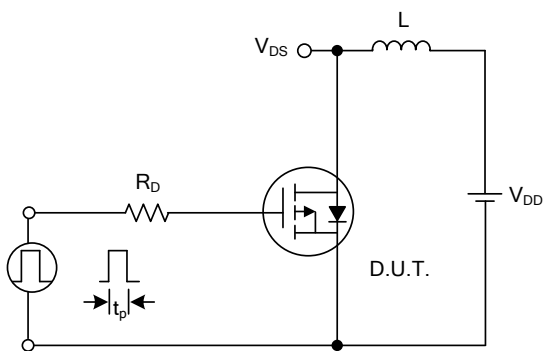
Switching Waveforms



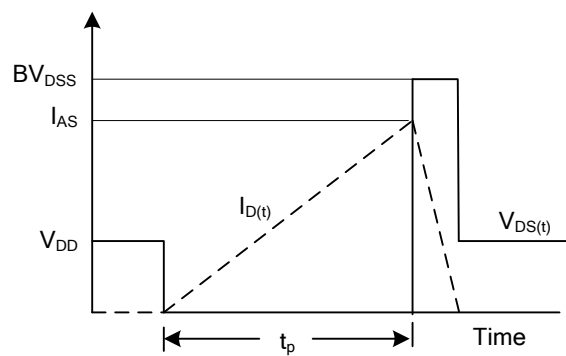
Gate Charge Test Circuit



Gate Charge Waveform

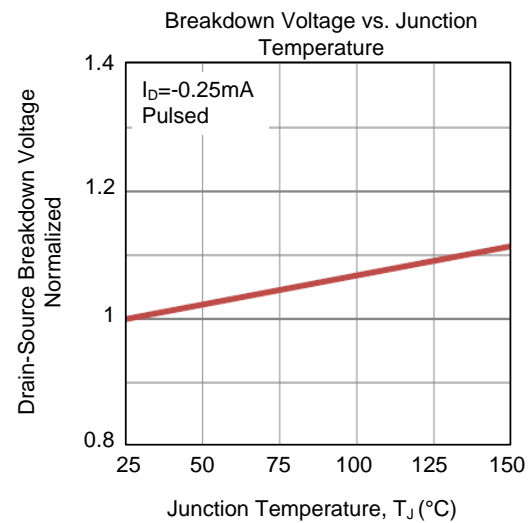
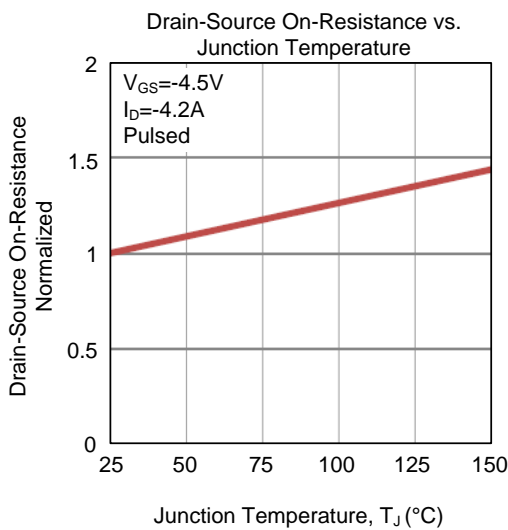
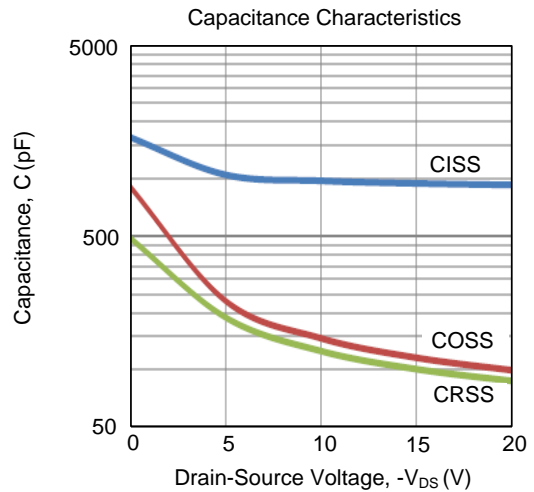
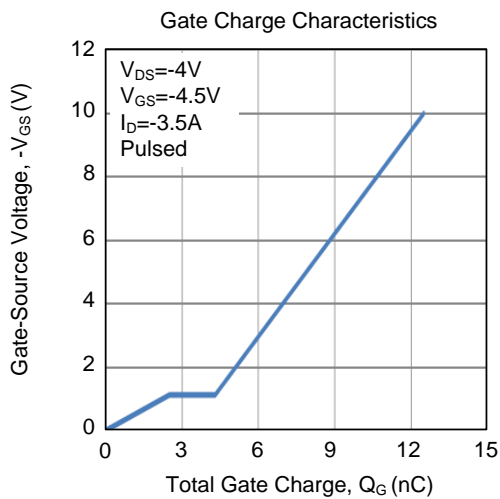
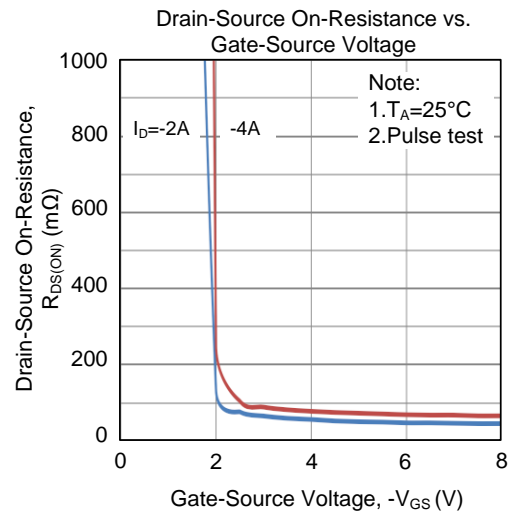
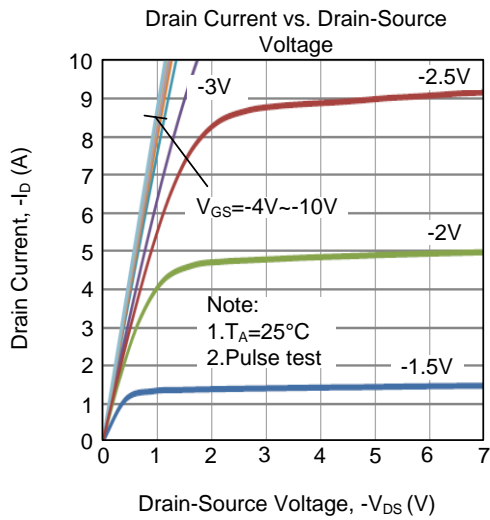


Unclamped Inductive Switching Test Circuit

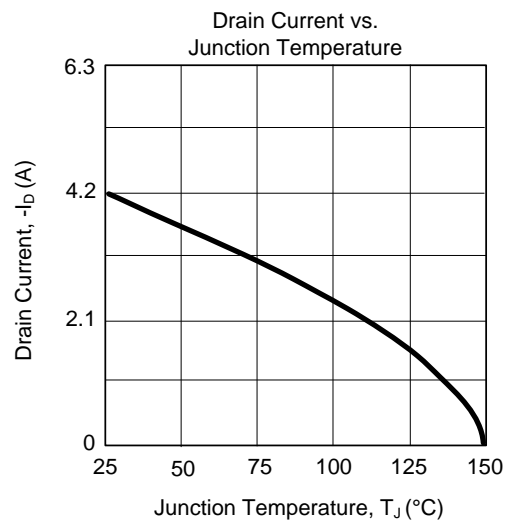
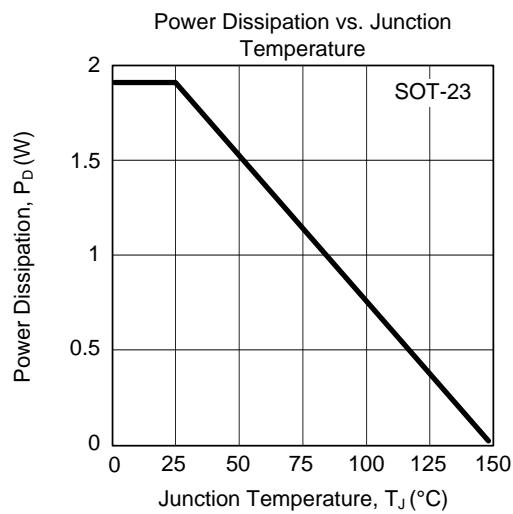
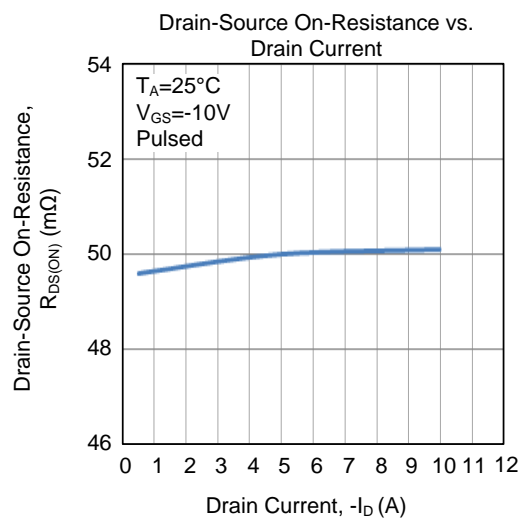
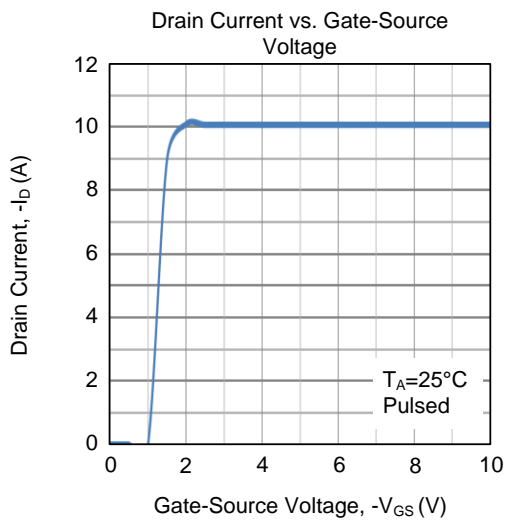
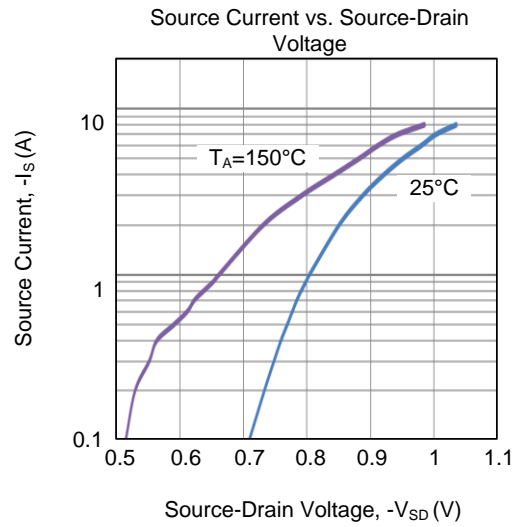
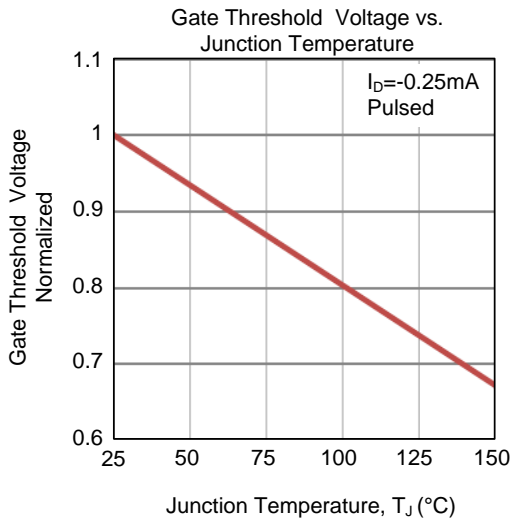


Unclamped Inductive Switching Waveforms

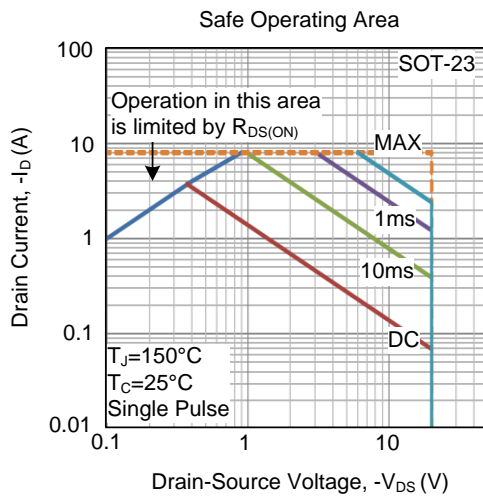
## ■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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