



UT3PP

Preliminary

DUAL TRANSISTOR

COMPOSITE TRANSISTORS

UT3PP

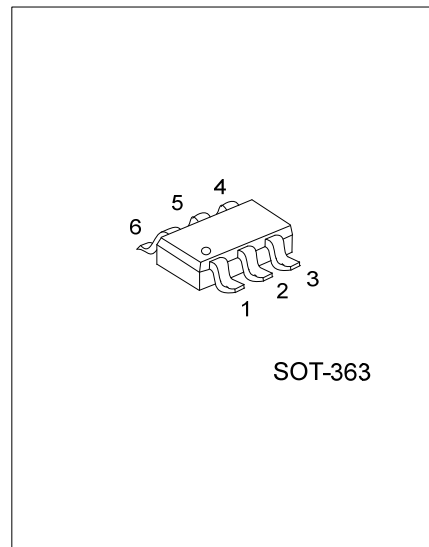
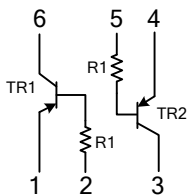
■ DESCRIPTION

As a composite transistor with resistor, the UTC **UT3PP** is for switching application.

■ FEATURES

- * Silicon Epitaxial Type
- * The Internal Tow Transistor Elements are Independent.

■ SYMBOL

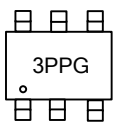


■ ORDERING INFORMATION

Ordering Number	Package	Pin Assignment						Packing
		1	2	3	4	5	6	
UT3PPG-AL6-R	SOT-363	E1	B1	C2	E2	B2	C1	Tape Reel

<p>UT3PPG-AL6-R</p>	<p>(1) R: Tape Reel</p> <p>(2) AL6: SOT-363</p> <p>(3) G: Halogen Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V_{CBO}	-50	V
Collector-Emitter Voltage	V_{CEO}	-50	V
Emitter-Base Voltage	V_{EBO}	-6	V
Collector Current	I_C	-100	mA
Peak Collector Current	I_{CM}	-200	mA
Collector Power dissipation	P_C	125	mW
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV_{CBO}	$I_C=-100\mu\text{A}$	-50			V
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	$I_C=-10\text{mA}$, $I_B=-0.5\text{mA}$			-0.3	V
Collector Cutoff Current	I_{CBO}	$V_{CB}=-50\text{V}$			-0.1	μA
DC Current Transfer Ratio	h_{FE}	$V_{CE}=-5\text{V}$, $I_C=-1\text{mA}$	100			
Transition Frequency	f_T	$V_{CE}=-6\text{V}$, $I_E=10\text{mA}$		150		MHz
Input Resistance	R_1		0.7	1.0	1.3	$\text{k}\Omega$

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