



02N60-TA

Power MOSFET

0.2A, 600V N-CHANNEL POWER MOSFET

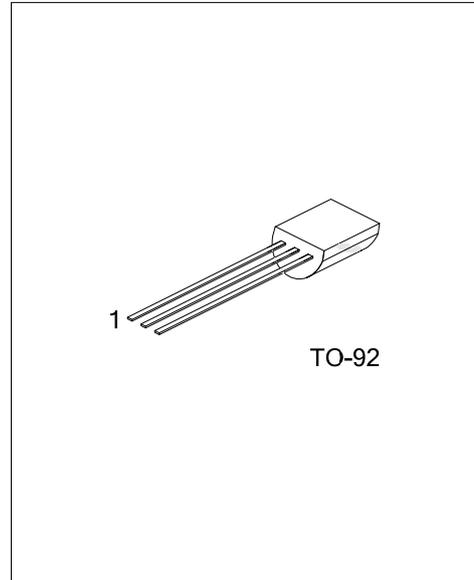
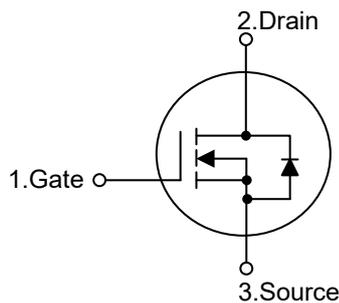
DESCRIPTION

The UTC **02N60-TA** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(on)} \leq 25 \Omega$ @ $V_{GS}=10V, I_D=0.1A$
- * High breakdown voltage

SYMBOL



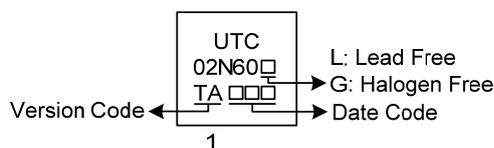
ORDERING INFORMATION

| Ordering Number | | Package | Pin Assignment | | | Packing |
|-----------------|-----------------|---------|----------------|---|---|----------|
| Lead Free | Halogen Free | | 1 | 2 | 3 | |
| 02N60L-TA-T92-B | 02N60G-TA-T92-B | TO-92 | G | D | S | Tape Box |
| 02N60L-TA-T92-K | 02N60G-TA-T92-K | TO-92 | G | D | S | Bulk |

Note: Pin Assignment: G: Gate D: Drain S: Source

| | |
|------------------------|---|
| <p>02N60G-TA-T92-B</p> | <p>(1) Packing Type (1) B: Tape Box, K: Bulk</p> <p>(2) Package Type (2) T92: TO-92</p> <p>(3) Version Code (3) Version TA</p> <p>(4) Green Package (4) G: Halogen Free and Lead Free, L: Lead Free</p> |
|------------------------|---|

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise specified)

| PARAMETER | | SYMBOL | RATINGS | UNIT |
|------------------------------------|------------|-----------|------------|------------------|
| Drain-Source Voltage | | V_{DSS} | 600 | V |
| Gate-Source Voltage | | V_{GSS} | ± 30 | V |
| Drain Current | Continuous | I_D | 0.2 | A |
| | Pulsed | I_{DM} | 1.2 | A |
| Peak Diode Recovery dv/dt (Note 3) | | dv/dt | 3.3 | V/ns |
| Power Dissipation | | P_D | 1 | W |
| Junction Temperature | | T_J | 150 | $^\circ\text{C}$ |
| Storage Temperature Range | | T_{STG} | -55 ~ +150 | $^\circ\text{C}$ |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $I_{SD} \leq 0.2\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, $T_J \leq T_{JMAX}$, $T_J = 25^\circ\text{C}$

■ THERMAL DATA

| PARAMETER | SYMBOL | RATINGS | UNIT |
|---------------------|---------------|-----------|---------------------------|
| Junction to Ambient | θ_{JA} | 160(Note) | $^\circ\text{C}/\text{W}$ |
| Junction to Case | θ_{JC} | 125(Note) | $^\circ\text{C}/\text{W}$ |

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

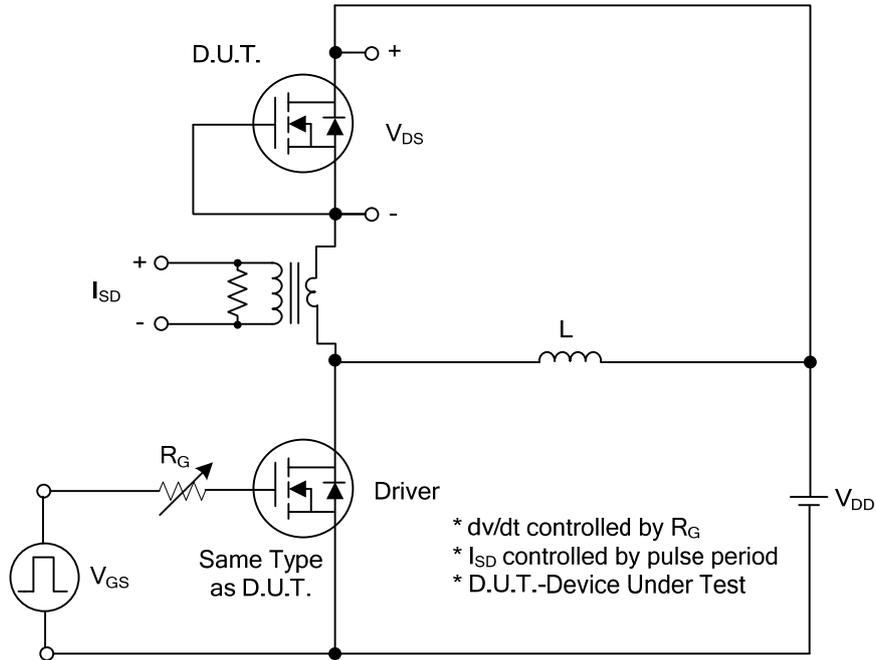
■ ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|--|--------------|--|-----|------|------|---------------|
| OFF CHARACTERISTICS | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$ | 600 | | | V |
| Drain-Source Leakage Current | I_{DSS} | $V_{DS}=600\text{V}$, $V_{GS}=0\text{V}$ | | | 10 | μA |
| Gate-Source Leakage Current | Forward | I_{GSS} $V_{GS}=+30\text{V}$, $V_{DS}=0\text{V}$ | | | +100 | nA |
| | Reverse | $V_{GS}=-30\text{V}$, $V_{DS}=0\text{V}$ | | | -100 | nA |
| ON CHARACTERISTICS | | | | | | |
| Gate Threshold Voltage | $V_{GS(TH)}$ | $V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$ | 2.0 | | 4.0 | V |
| Static Drain-Source On-State Resistance | $R_{DS(ON)}$ | $V_{GS}=10\text{V}$, $I_D=0.1\text{A}$ | | | 25 | Ω |
| DYNAMIC PARAMETERS | | | | | | |
| Input Capacitance | C_{ISS} | $V_{GS}=0\text{V}$, $V_{DS}=25\text{V}$, $f=1.0\text{MHz}$ | | 58 | | pF |
| Output Capacitance | C_{OSS} | | | 11.8 | | pF |
| Reverse Transfer Capacitance | C_{RSS} | | | 2 | | pF |
| SWITCHING PARAMETERS | | | | | | |
| Total Gate Charge | Q_G | $V_{DS}=480\text{V}$, $V_{GS}=10\text{V}$, $I_D=0.2\text{A}$, (Note 1, 2) | | 8 | | nC |
| Gate to Source Charge | Q_{GS} | | | 1 | | nC |
| Gate to Drain Charge | Q_{GD} | | | 3 | | nC |
| Turn-ON Delay Time | $t_{D(ON)}$ | $V_{DS}=100\text{V}$, $V_{GS}=10\text{V}$, $I_D=0.2\text{A}$, $R_G=25\Omega$ (Note 1, 2) | | 3.2 | | ns |
| Rise Time | t_R | | | 17 | | ns |
| Turn-OFF Delay Time | $t_{D(OFF)}$ | | | 24 | | ns |
| Fall-Time | t_F | | | 175 | | ns |
| SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS | | | | | | |
| Maximum Body-Diode Continuous Current | I_S | | | | 0.2 | A |
| Maximum Body-Diode Pulsed Current | I_{SM} | | | | 1.2 | A |
| Drain-Source Diode Forward Voltage | V_{SD} | $I_S=0.2\text{A}$, $V_{GS}=0\text{V}$ | | | 1.4 | V |
| Reverse Recovery Time | t_{RR} | $V_{GS}=0\text{V}$, $I_S=0.2\text{A}$, $di/dt=100\text{A}/\mu\text{s}$ | | 92 | | ns |
| Reverse Recovery Charge | Q_{RR} | | | 110 | | nC |

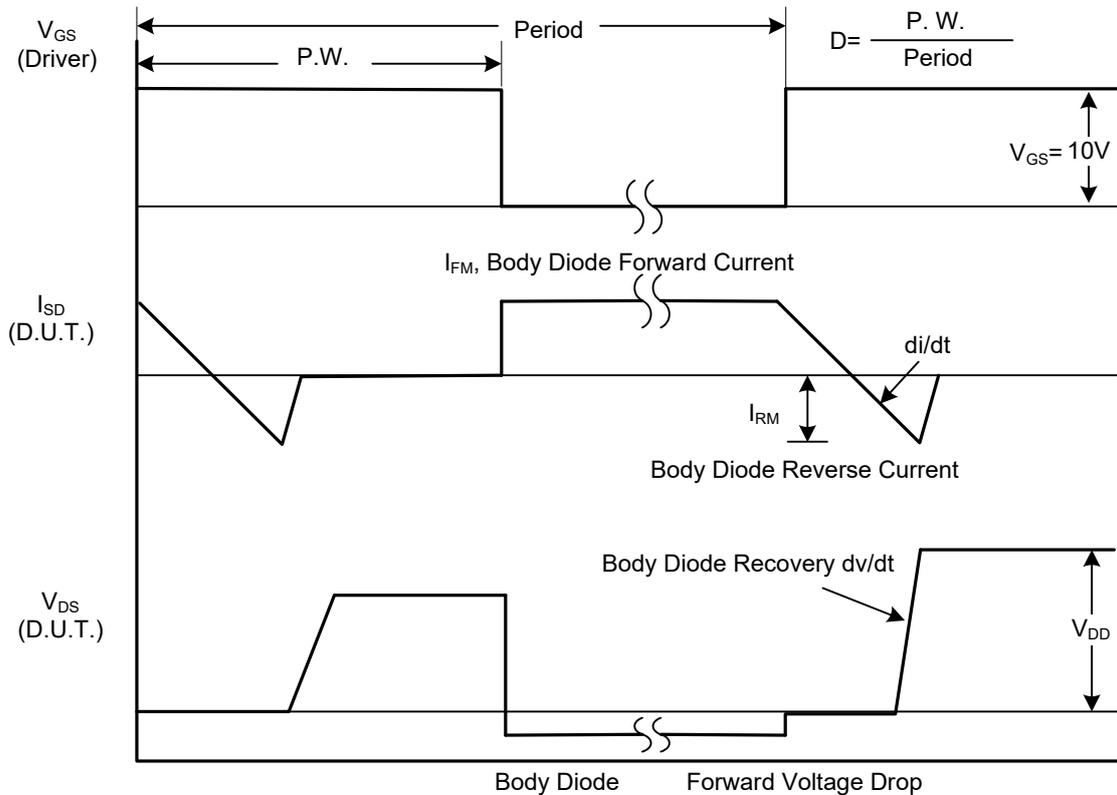
Notes: 1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

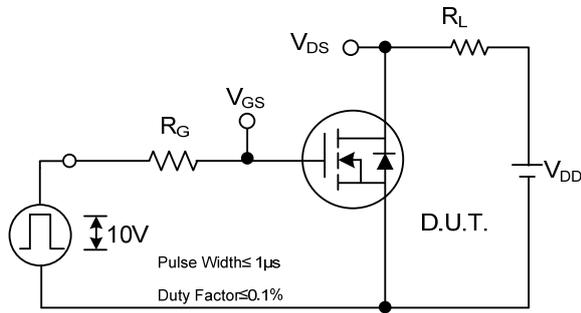


Peak Diode Recovery dv/dt Test Circuit

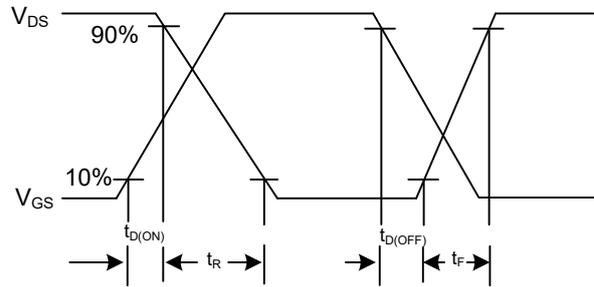


Peak Diode Recovery dv/dt Waveforms

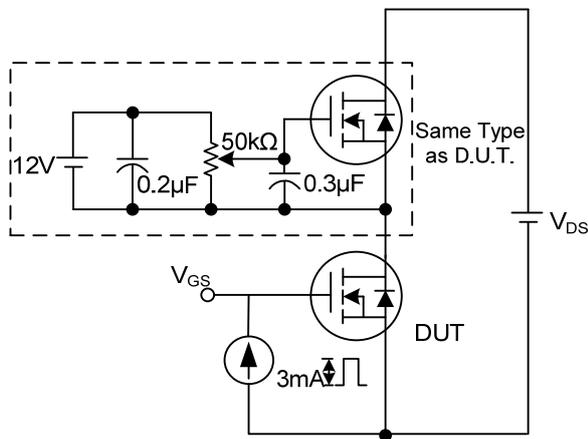
TEST CIRCUITS AND WAVEFORMS



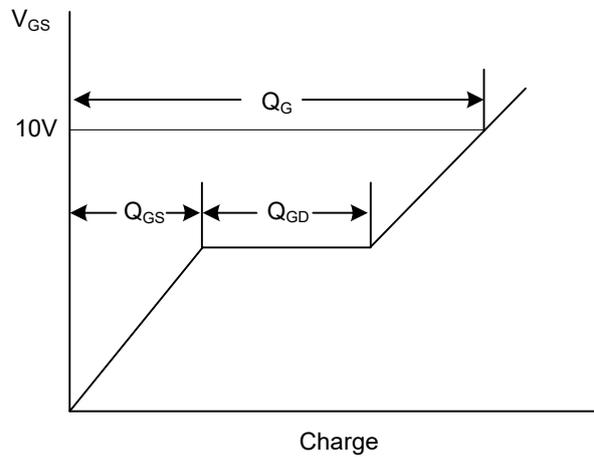
Switching Test Circuit



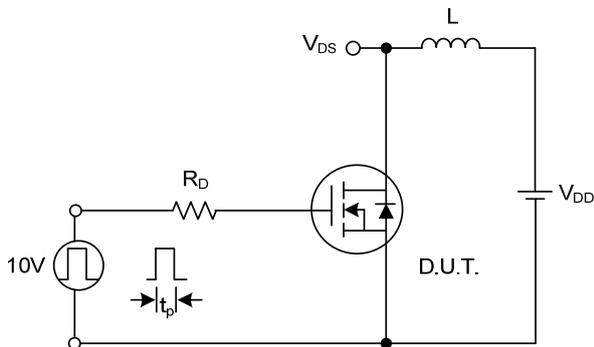
Switching Waveforms



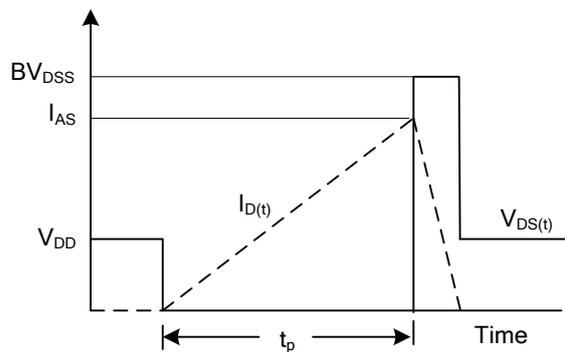
Gate Charge Test Circuit



Gate Charge Waveform

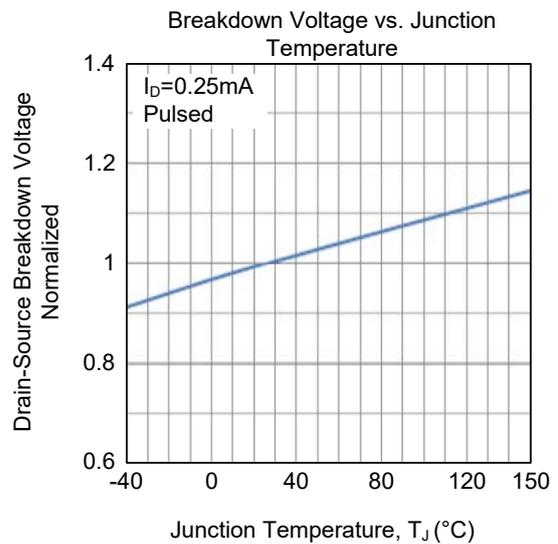
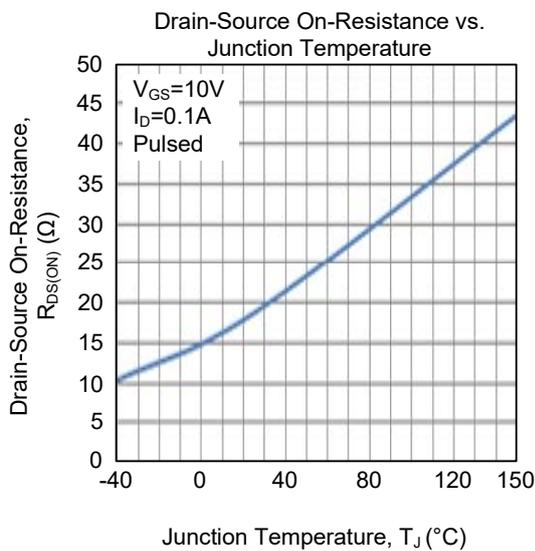
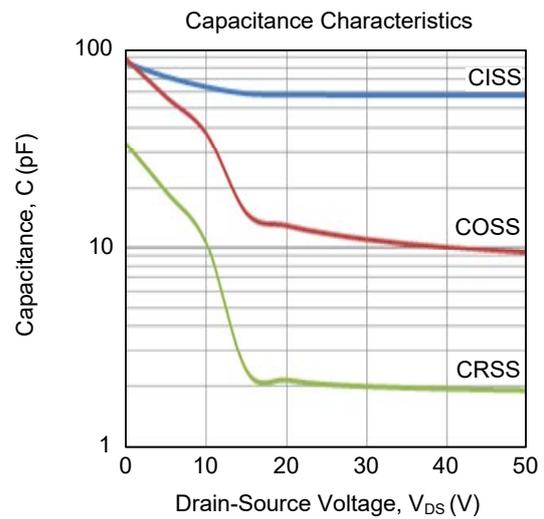
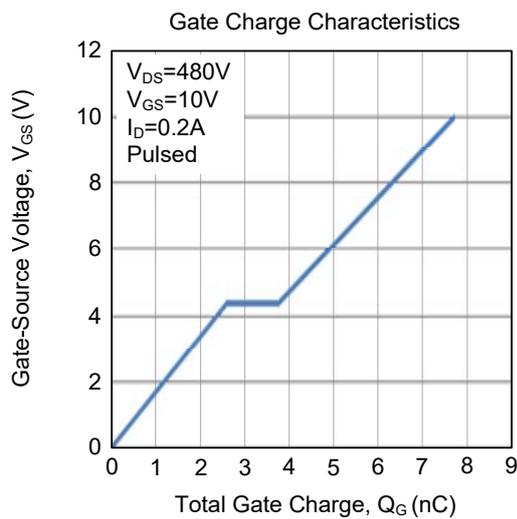
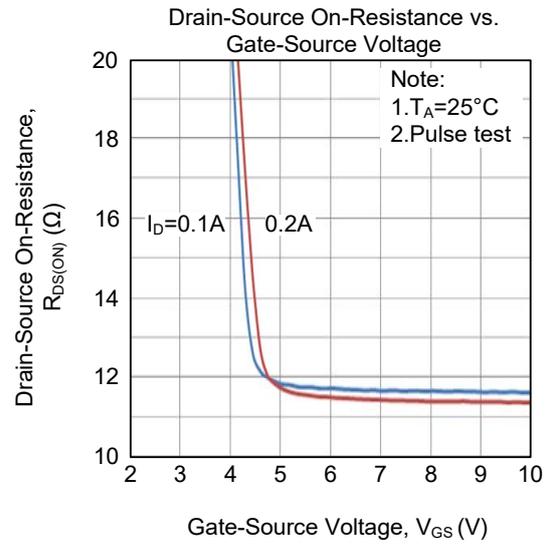
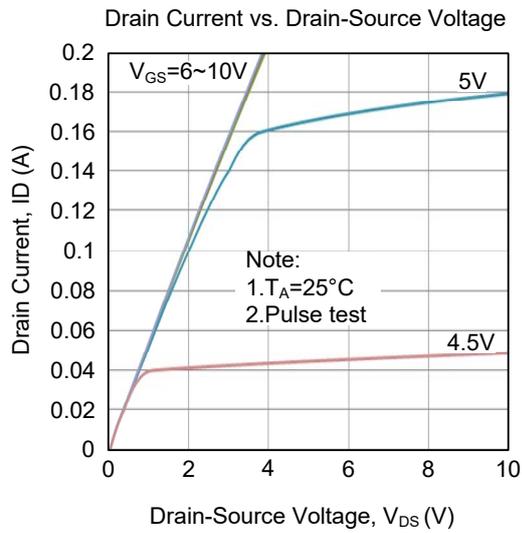


Unclamped Inductive Switching Test Circuit

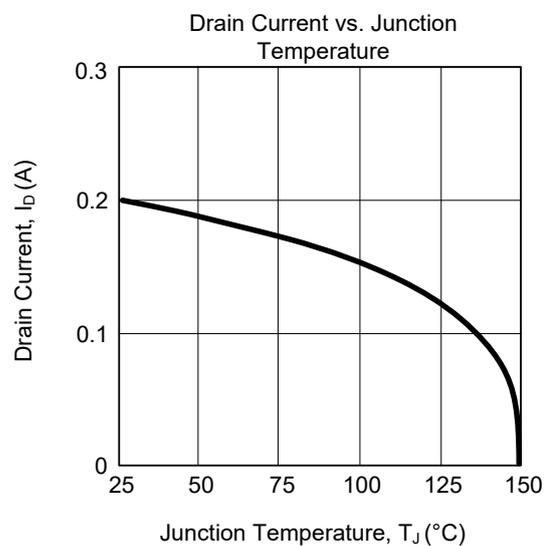
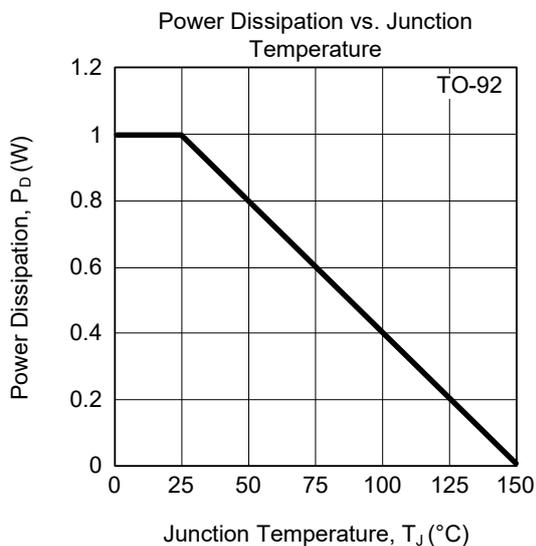
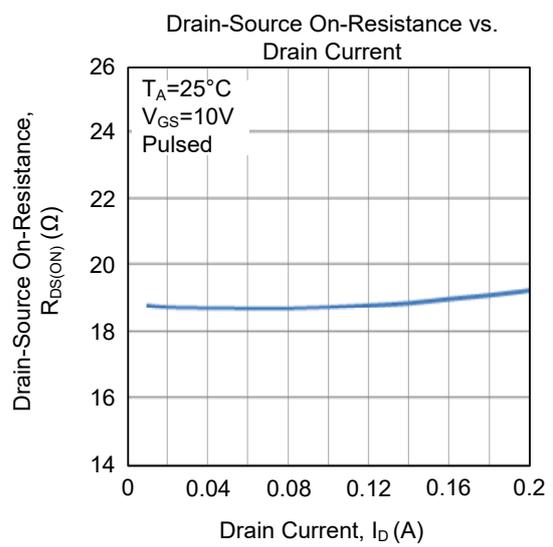
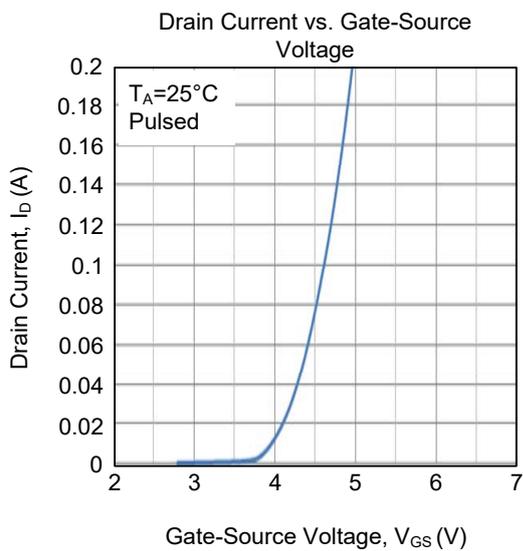
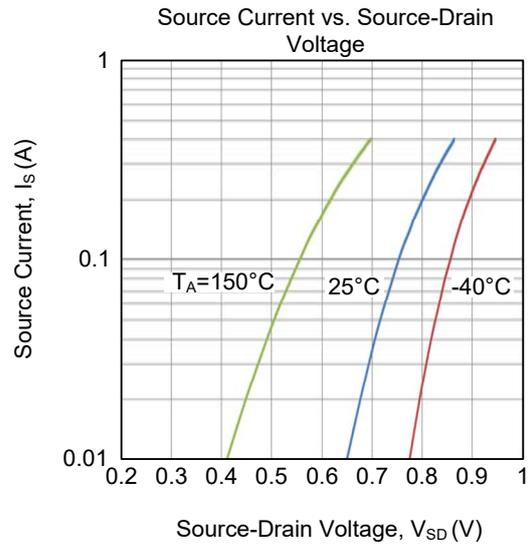
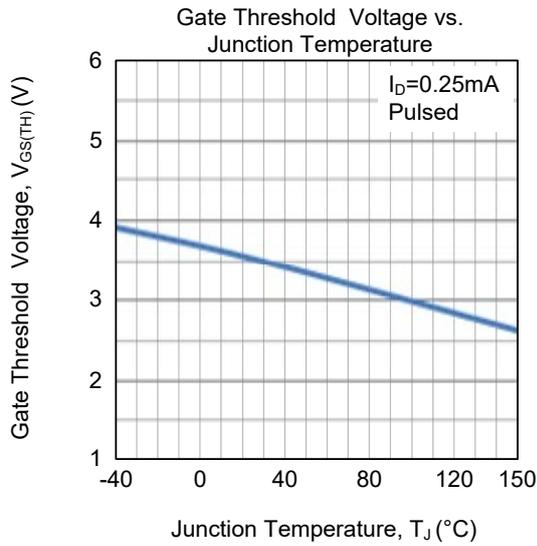


Unclamped Inductive Switching Waveforms

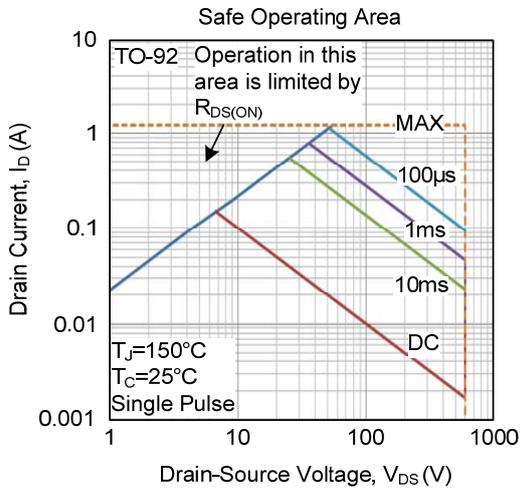
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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