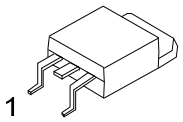
USG085R035H-T

Preliminary ***POWER MOSFET***

100A, 85V N-CHANNEL POWER MOSFET



TO-263

PDFN5×6

■ DESCRIPTION

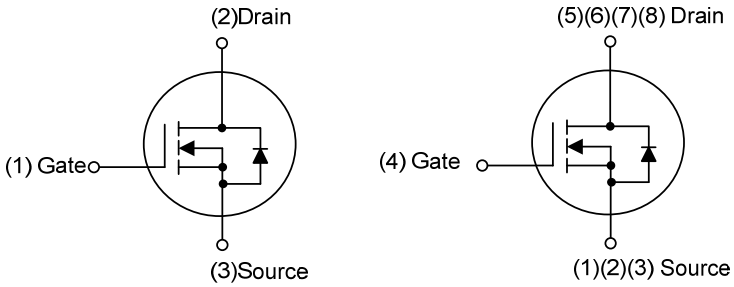
The UTC **USG085R035H-T** is a N-channel Power MOSFET, it uses UTC's advanced technology to provide the customers with low $R_{DS(ON)}$ characteristic by high cell density trench technology.

The UTC **USG085R035H-T** is suitable for high efficiency synchronous rectification in SMPS, UPS, hard switched and high frequency circuits.

■ FEATURES

* $R_{DS(ON)} \leq 3.5\ m\Omega$ @ V_{GS}=10V, I_D=50A
* High Switching Speed

■ SYMBOL



TO-263 PDFN5×6

■ ORDERING INFORMATION

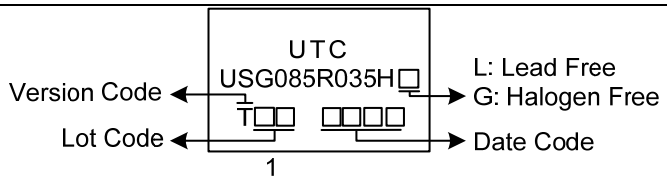
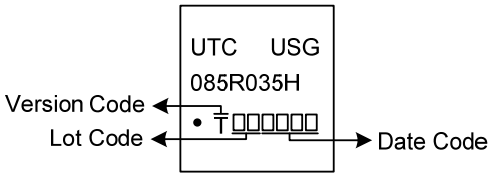
Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
USG085R035HL-T-TQ2-T	USG085R035HG-T-TQ2-T	TO-263	G	D	S	-	-	-	-	-	Tube
USG085R035HL-T-TQ2-R	USG085R035HG-T-TQ2-R	TO-263	G	D	S	-	-	-	-	-	Tape Reel
USG085R035HL-T-P5060-R	USG085R035HG-T-P5060-R	PDFN5×6	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: S: Source G: Gate D: Drain

USG085R035HG-T-TQ2-T

(1)Packing Type	(1) T: Tube, R: Tape Reel
(2)Package Type	(2) TQ2: TO-263, P5060: PDFN5×6
(3)Version Code	(3) Version T
(4)Green Package	(4) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING

PACKAGE	MARKING
TO-263	
PDFN5×6	

■ ABSOLUTE MAXIMUM RATING (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	85	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous	I_D	100	A
	Pulsed (Note 2)	I_{DM}	200	A
Single Pulsed Avalanche Energy (Note 3)		E_{AS}	118	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.55	V/ns
Power Dissipation	TO-263	P_D	138	W
	PDFN5×6		100	W
Junction Temperature		T_J	+150	°C
Storage Temperature Range		T_{STG}	-20 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L= 0.1mH, I_{AS} = 49A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C
4. I_{SD} ≤ 50A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-263	θ_{JA}	62.5	°C/W
	PDFN5×6		65	°C/W
Junction to Case	TO-263	θ_{JC}	0.9	°C/W
	PDFN5×6		1.25	°C/W

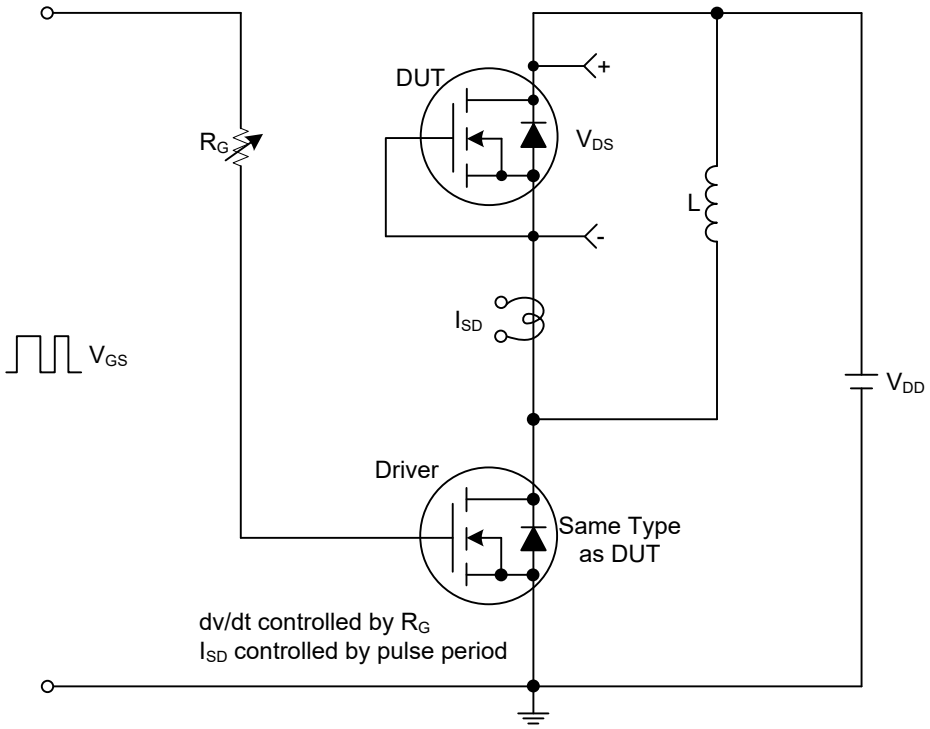
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

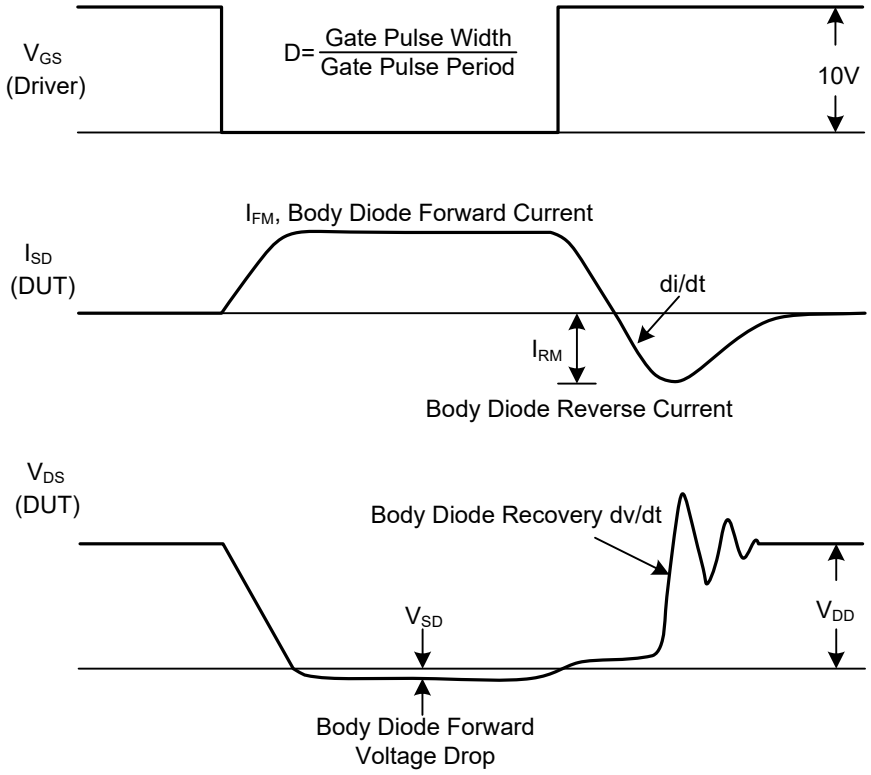
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	85			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=80V, V_{GS}=0V			1	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-20V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=50A			3.5	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		5965		pF
Output Capacitance		C_{OSS}			2155		pF
Reverse Transfer Capacitance		C_{RSS}			106		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=68V, V_{GS}=10V, I_D=100A, (Note 1, 2)		140		nC
Gate to Source Charge		Q_{GS}			40		nC
Gate to Drain Charge		Q_{GD}			36		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=40V, V_{GS}=10V, I_D=100A, R_G =3.3Ω (Note 1, 2)		23		ns
Rise Time		t_R			21		ns
Turn-off Delay Time		$t_{D(OFF)}$			55		ns
Fall-Time		t_F			25		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				100	A
Maximum Body-Diode Pulsed Current		I_{SM}				200	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=100A, V_{GS}=0V			1.4	V
Reverse Recovery Time (Note 1)		t_{rr}	I_S=30A, V_{GS}=0V, dI_F/dt =85A/μs		66		nS
Reverse Recovery Charge		Q_{rr}			125		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS



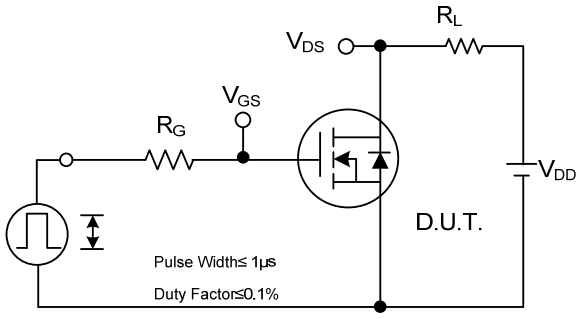
Peak Diode Recovery dv/dt Test Circuit



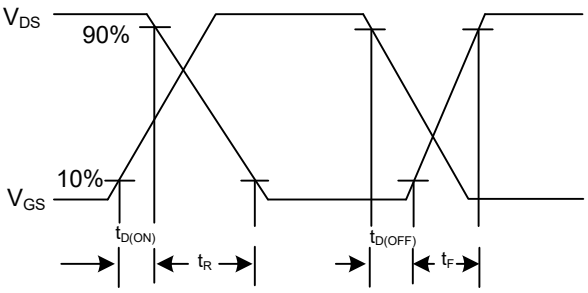
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

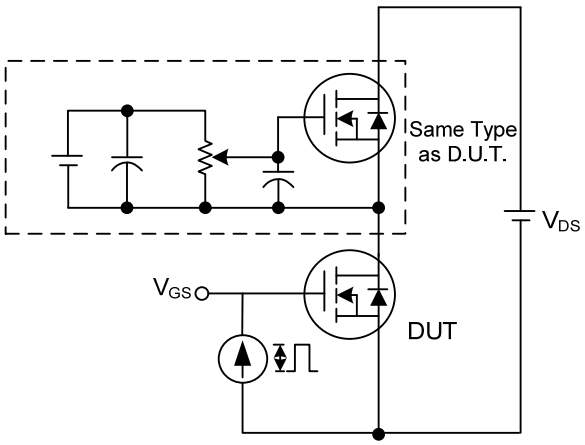
■ TEST CIRCUITS AND WAVEFORMS



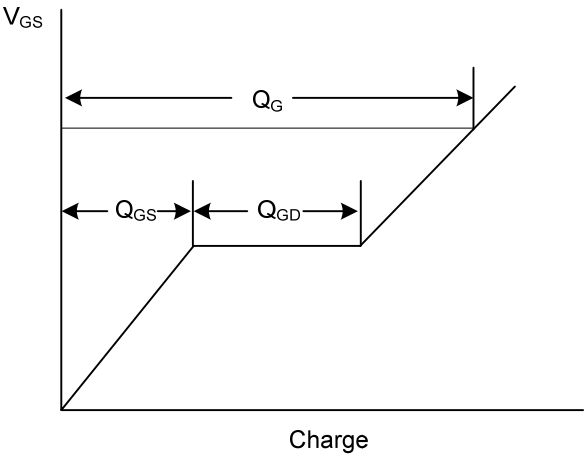
Switching Test Circuit



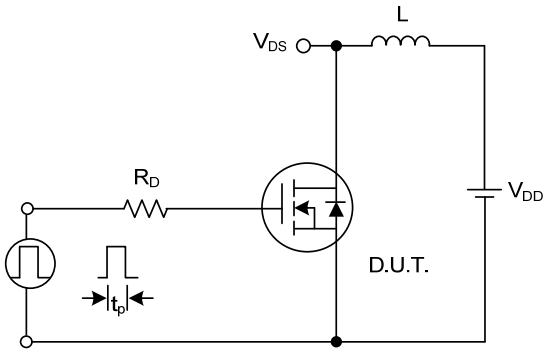
Switching Waveforms



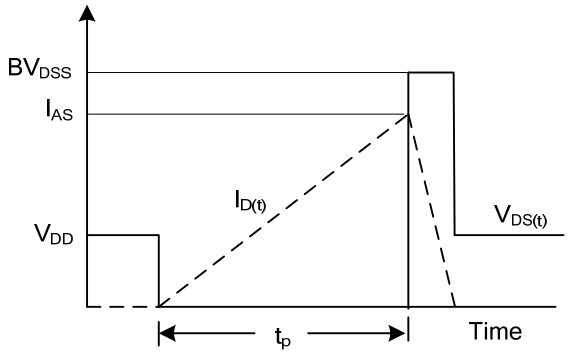
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms