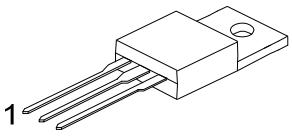
100A, 70V N-CHANNEL POWER MOSFET



TO-220

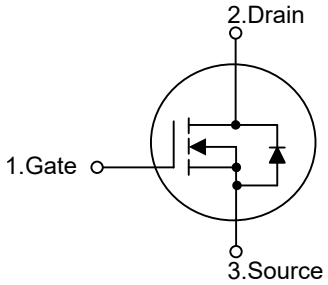
■ DESCRIPTION

The UTC **UT100N07H** is an N-channel enhancement mode Power FET, it uses UTC's advanced technology to provide customers a minimum on-state resistance and high switching speed.

■ FEATURES

* $R_{DS(ON)}$ ≤ 7.0 mΩ @ V_{GS}=10V, I_D=50A
* High switching speed
* Improved dv/dt capability

■ SYMBOL



■ ORDERING INFORMATION

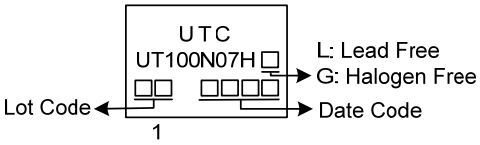
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT100N07HL-TA3-T	UT100N07HG-TA3-T	TO-220	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

UT100N07HG-TA3-T

(1) Packing Type	(1) T: Tube
(2) Package Type	(2) TA3: TO-220
(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	70	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous	I_D	100	A
	Pulsed	I_{DM}	200	A
Avalanche Energy	Single Pulsed	E_{AS}	125	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.5	V/ns
Power Dissipation		P_D	200	W
Junction Temperature		T_J	+150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 0.1mH, I_{AS} = 50A, V_{DD} = 30V, R_G = 25Ω, Starting T_J = 25°C
4. I_{SD} ≤ 30A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

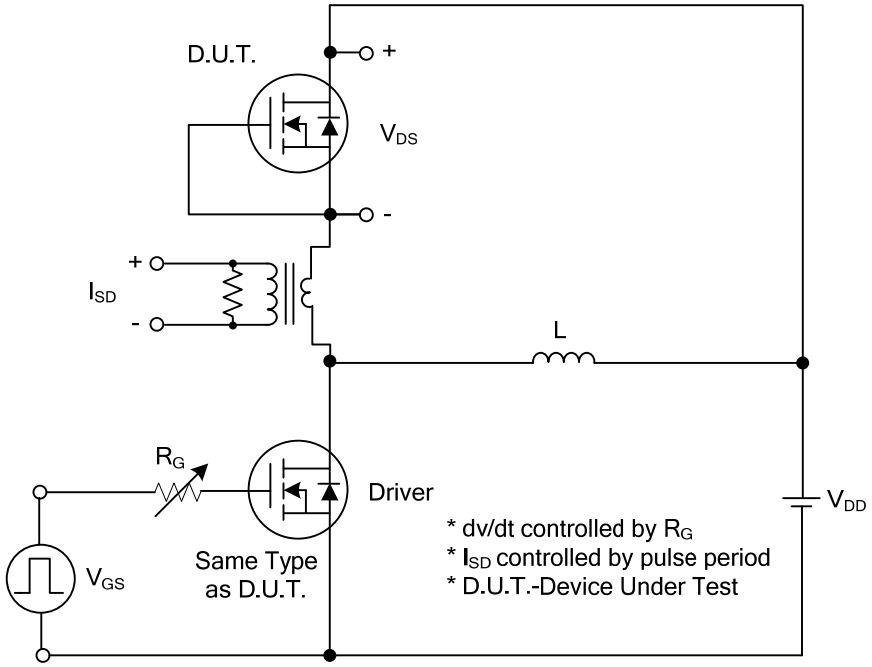
PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ_{JC}	0.625	°C/W

■ ELECTRICAL CHARACTER ISTICS (T_J=25°C, unless otherwise specified)

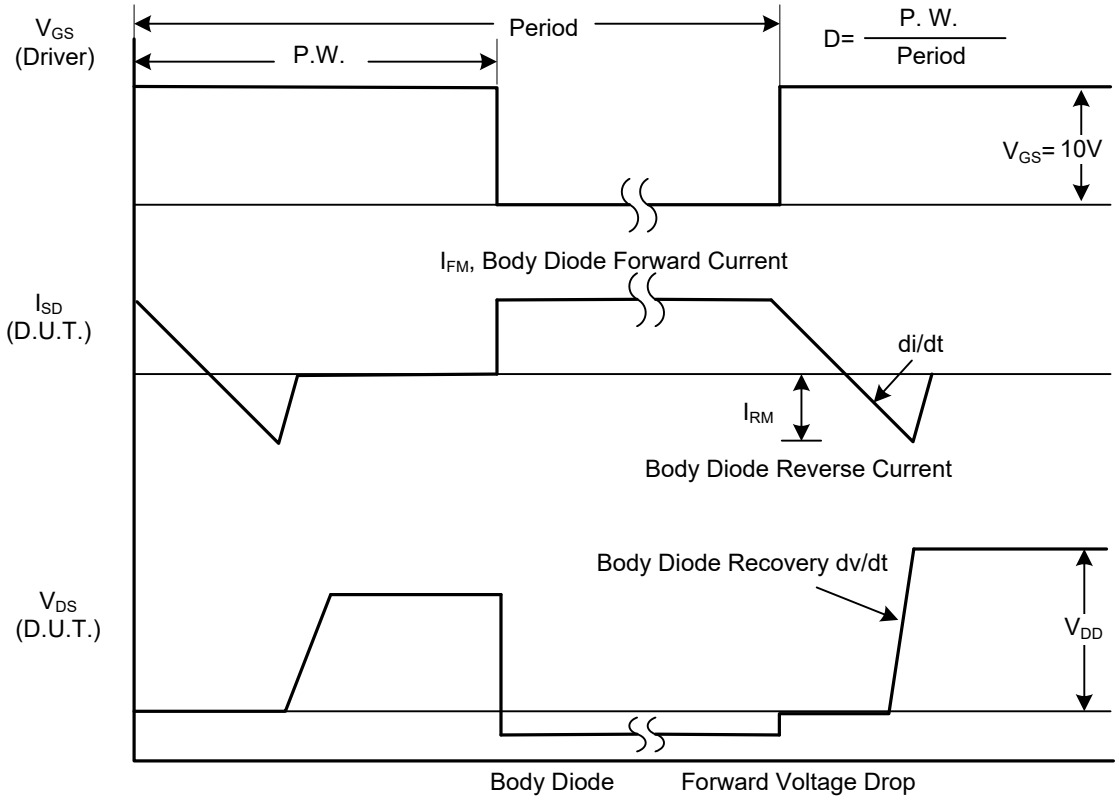
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	70			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=70V,V_{GS}=0V			1	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-20V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	I_D=250μA, V_{DS}=V_{GS}	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=50A			7.0	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1MHz		14300		pF
Output Capacitance		C_{OSS}			1460		pF
Reverse Transfer Capacitance		C_{RSS}			1270		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{DD}=56V, V_{GS}=10V, I_D=100A, (Note 1, 2)		145		nC
Gate to Source Charge		Q_{GS}			40		nC
Gate to Drain Charge		Q_{GD}			60		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=35V, V_{GS}=10V I_D=100A, R_G=3Ω (Note 1, 2)		18		ns
Rise Time		t_R			19		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			34		ns
Fall-Time		t_F			20		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				100	A
Maximum Body-Diode Pulsed Current		I_{SM}				200	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=100A			1.4	V
Reverse Recovery Time		t_{rr}	I_S=30A, V_{GS}=0V		60		nS
Reverse Recovery Charge (Note 1)		Q_{rr}	dI_F/dt=100A/μs		105		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

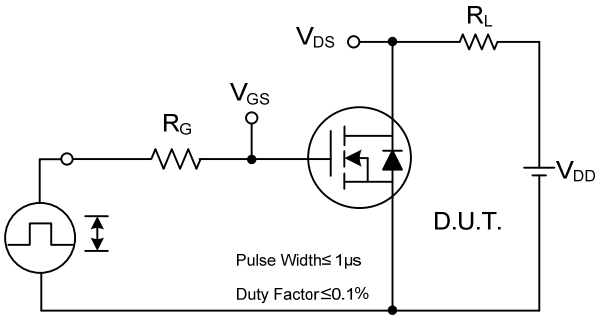


Peak Diode Recovery dv/dt Test Circuit

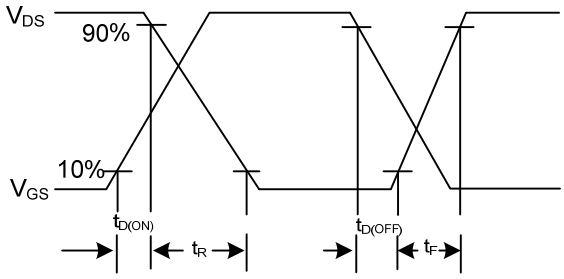


Peak Diode Recovery dv/dt Waveforms

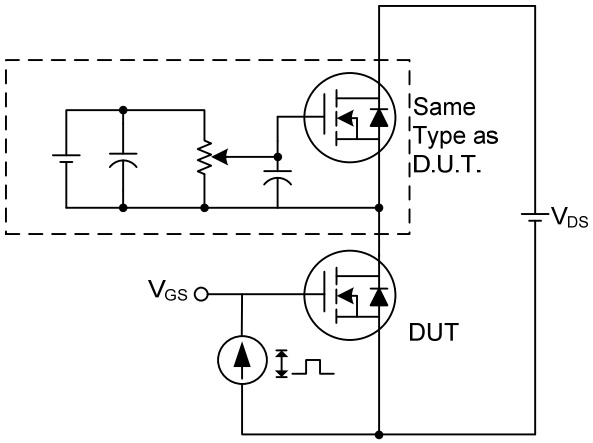
■ TEST CIRCUITS AND WAVEFORMS



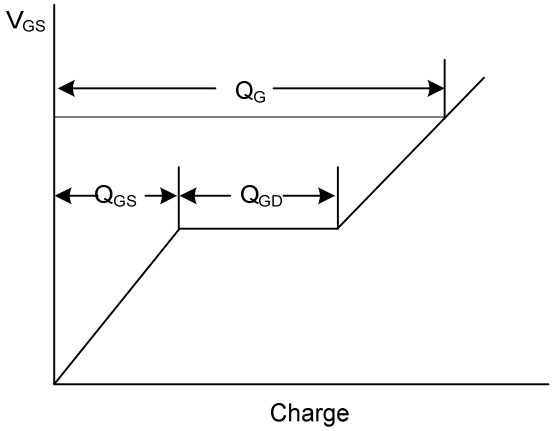
Switching Test Circuit



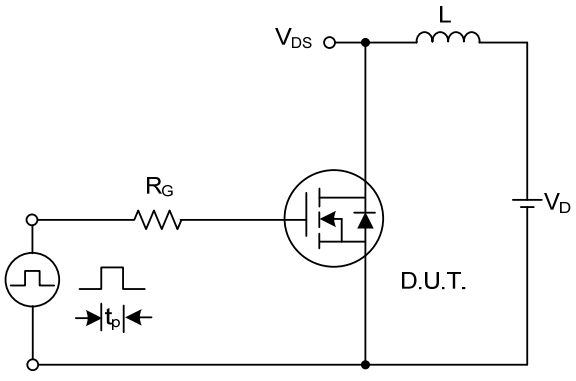
Switching Waveforms



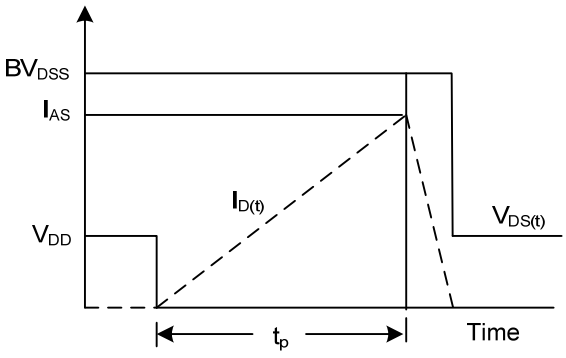
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms